

FIG. 1

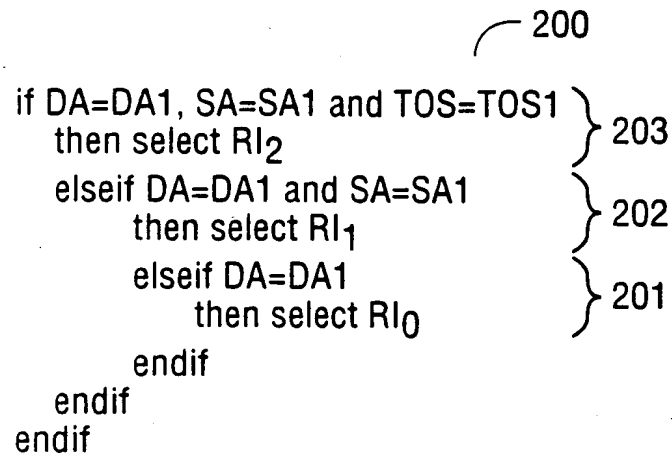


FIG. 2

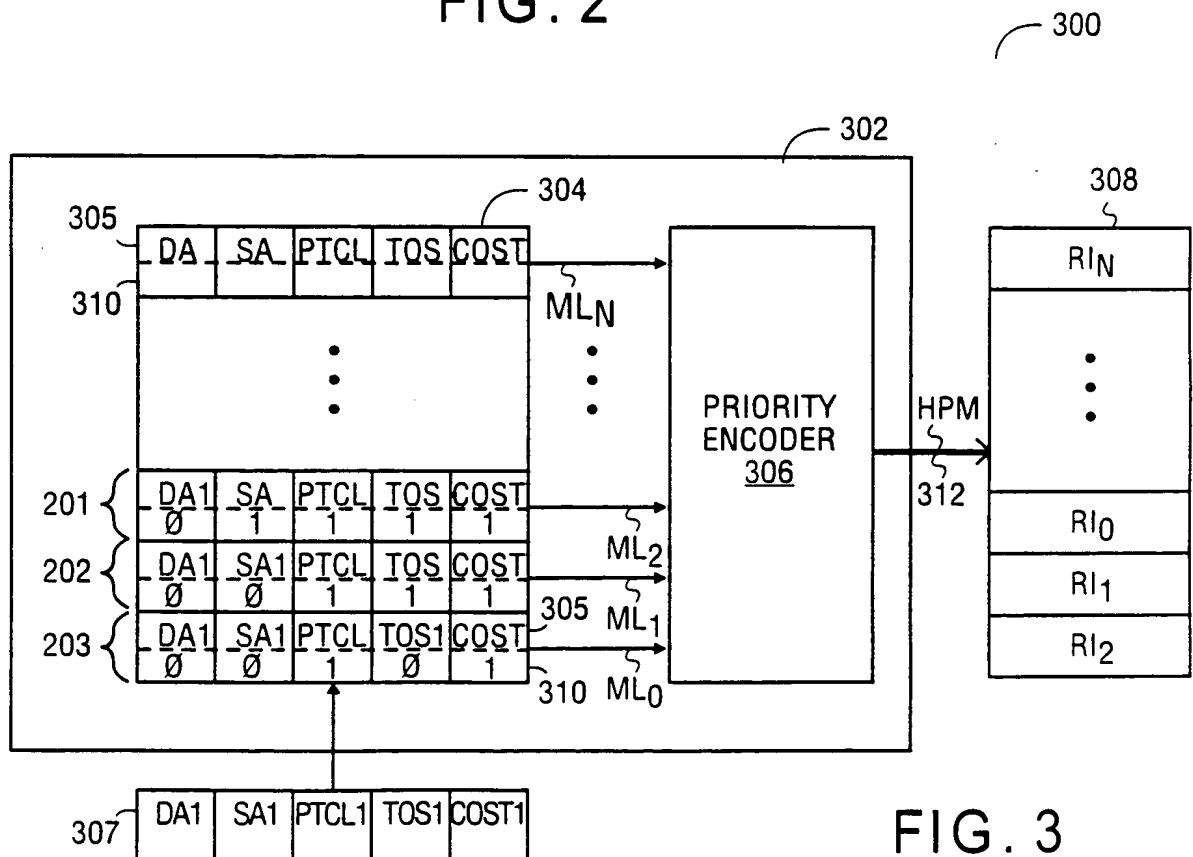


FIG. 3

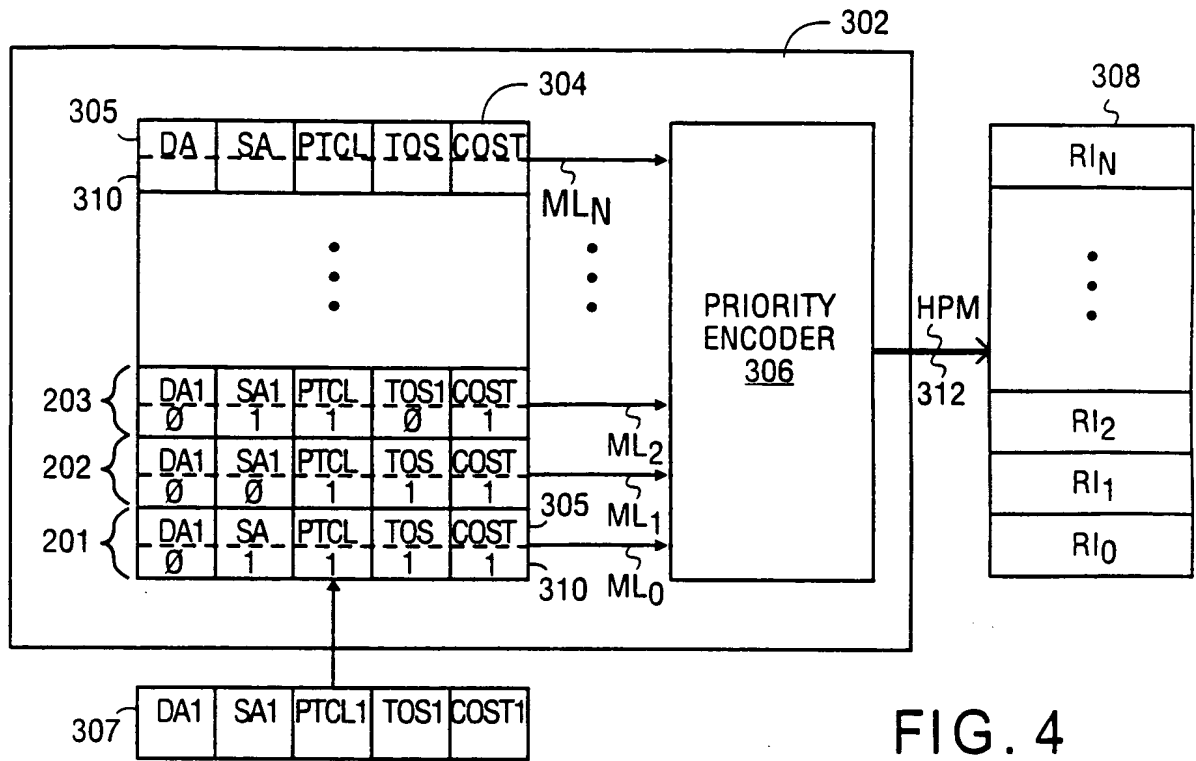


FIG. 4

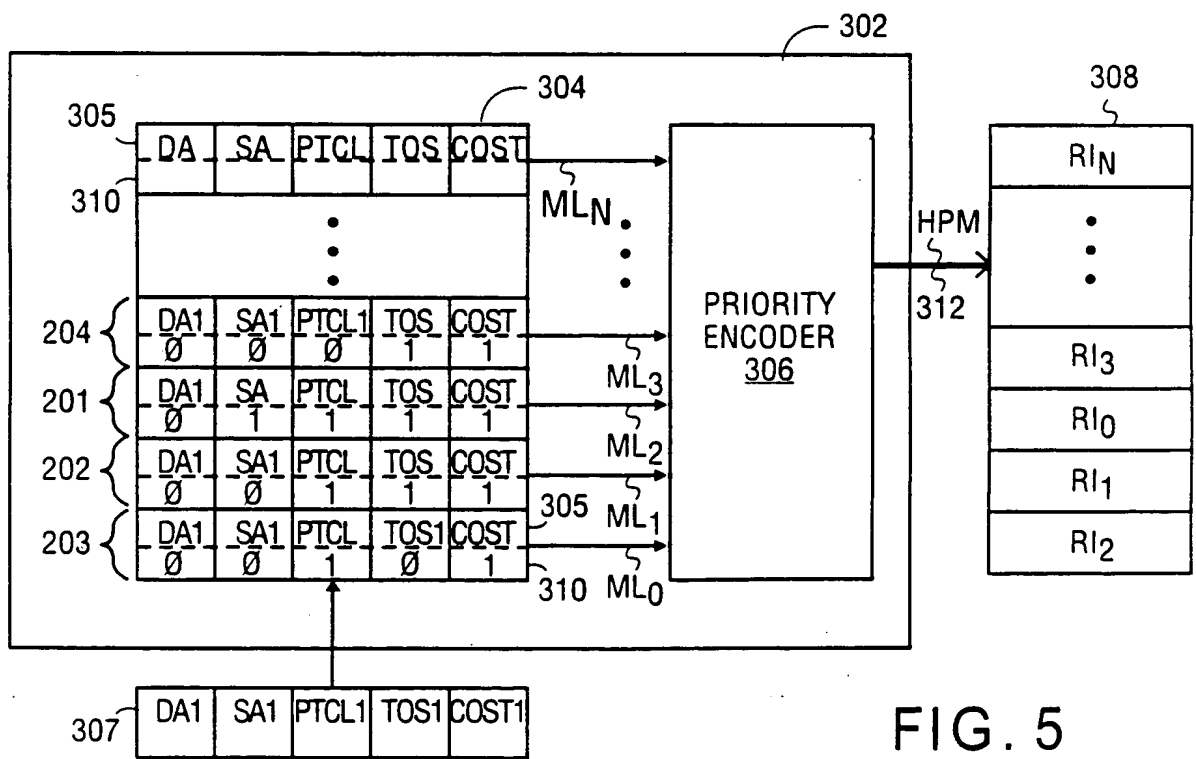


FIG. 5

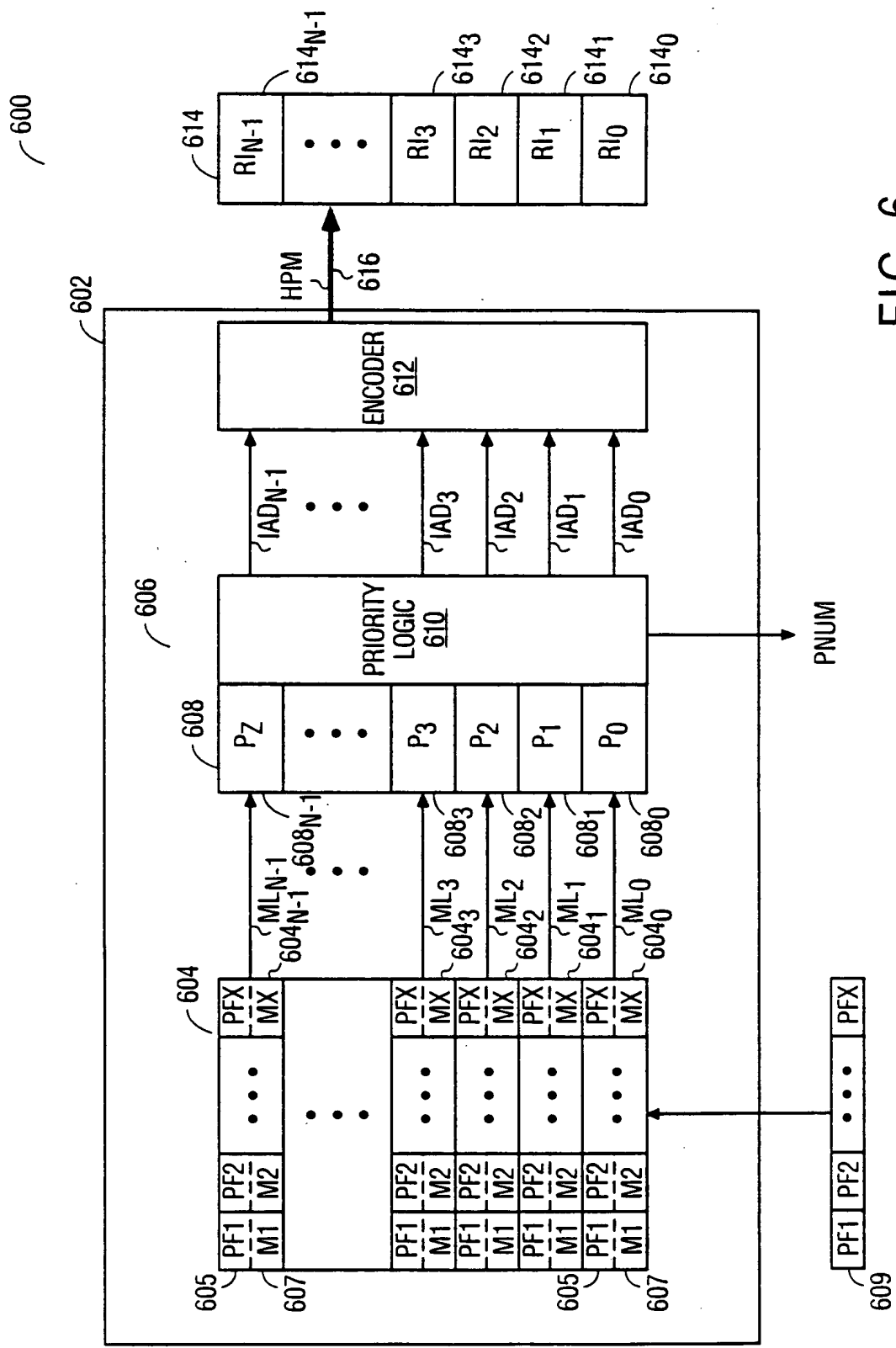


FIG. 6

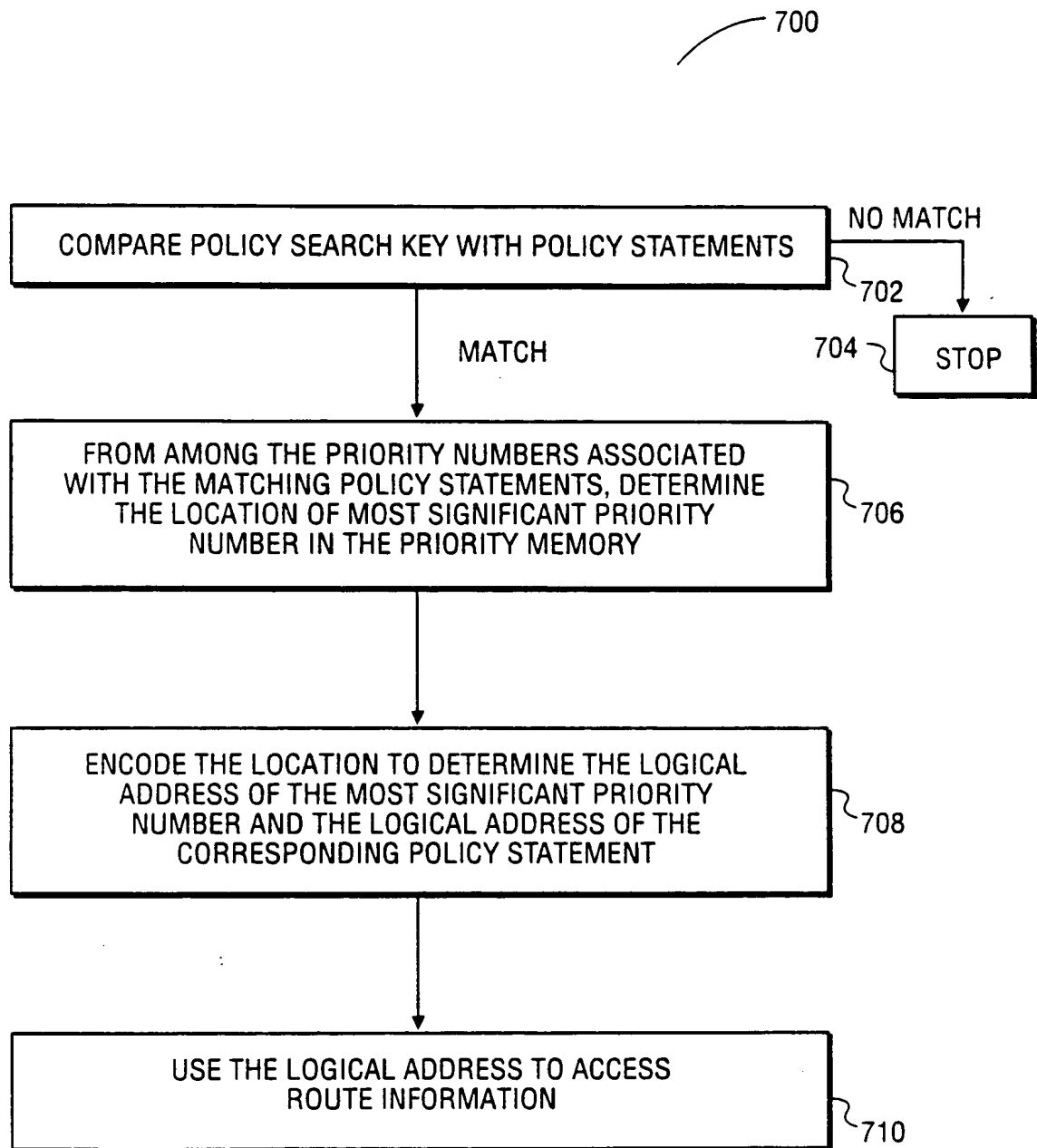
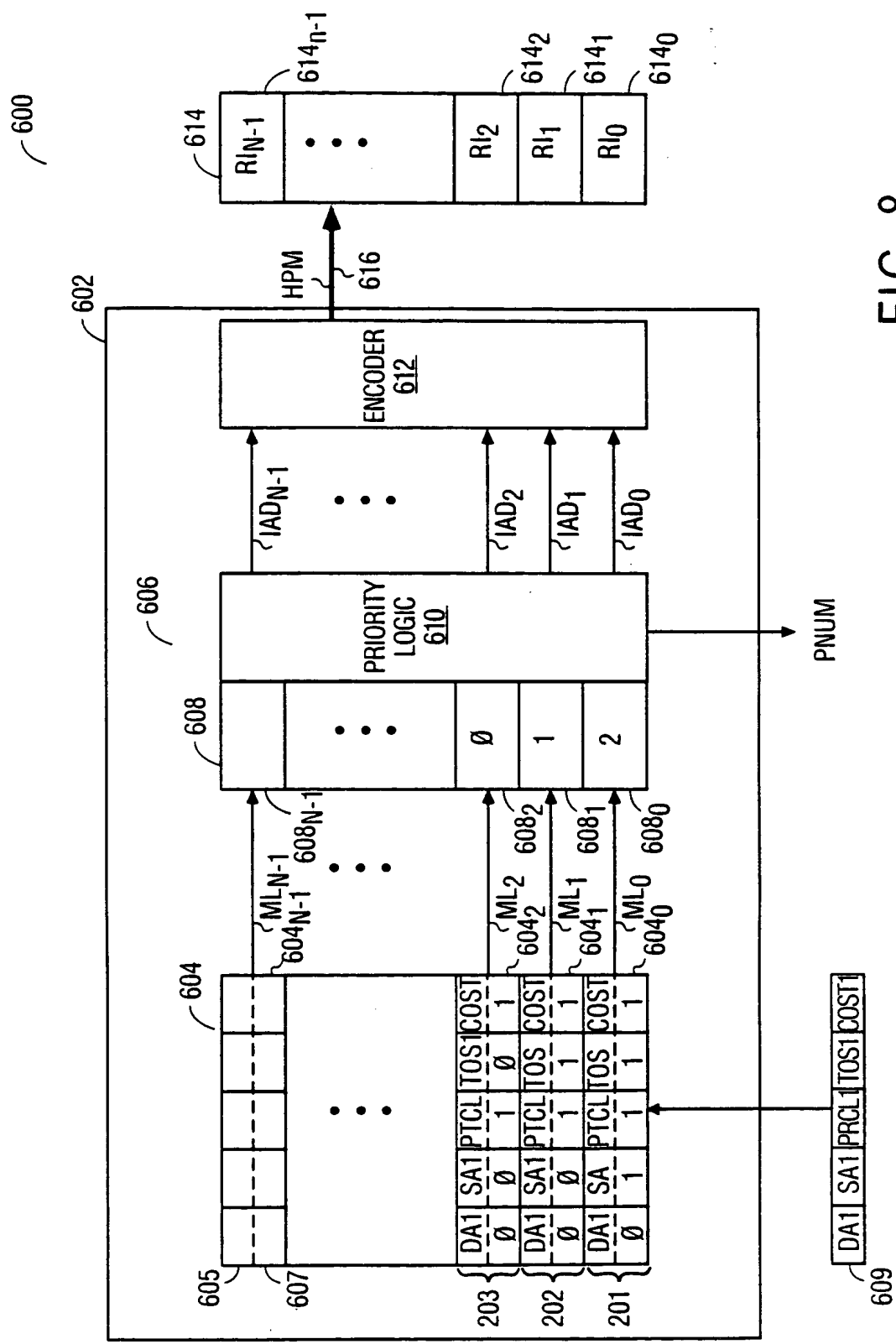


FIG. 7



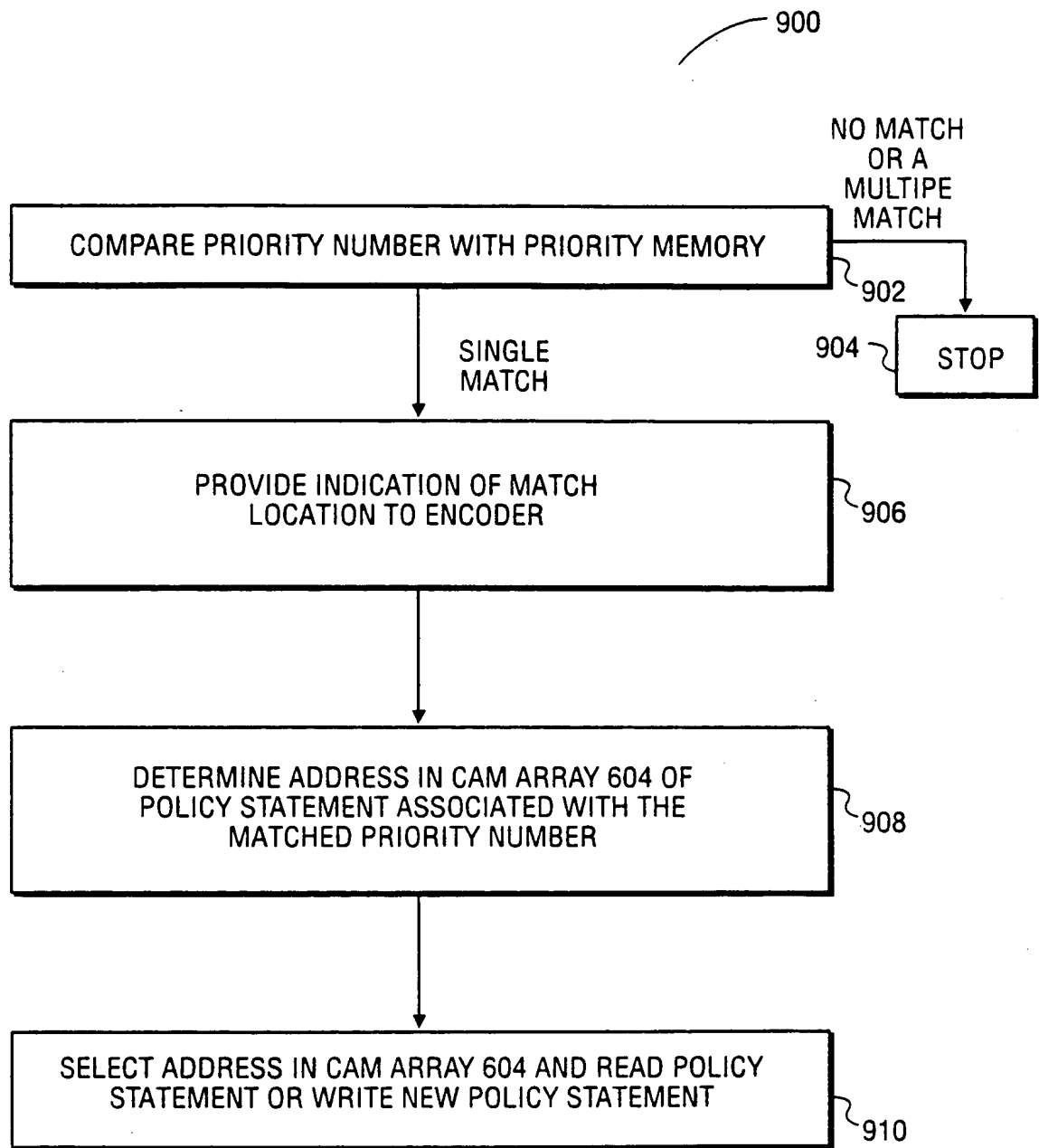


FIG. 9

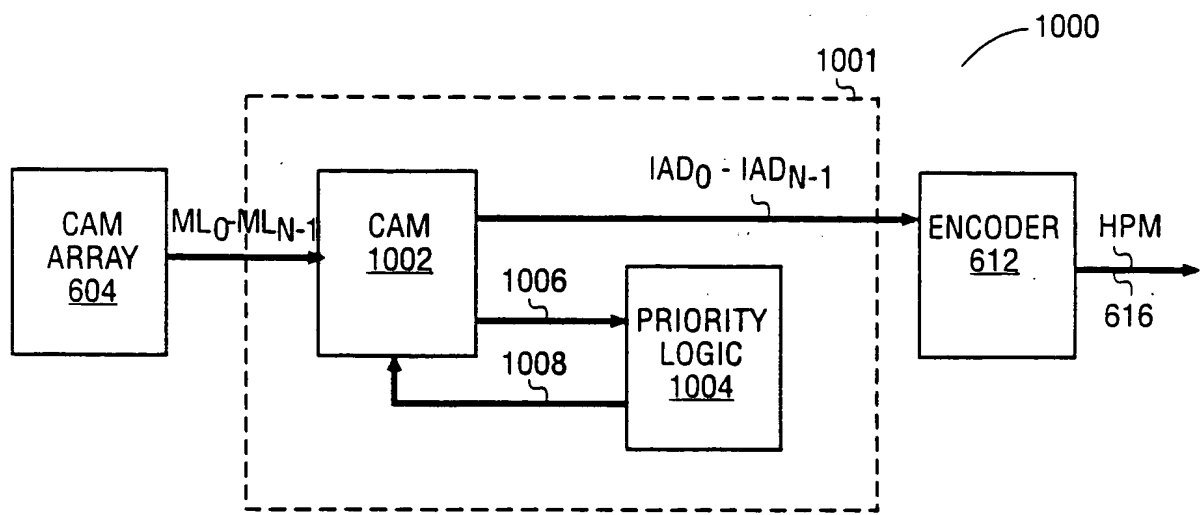


FIG. 10

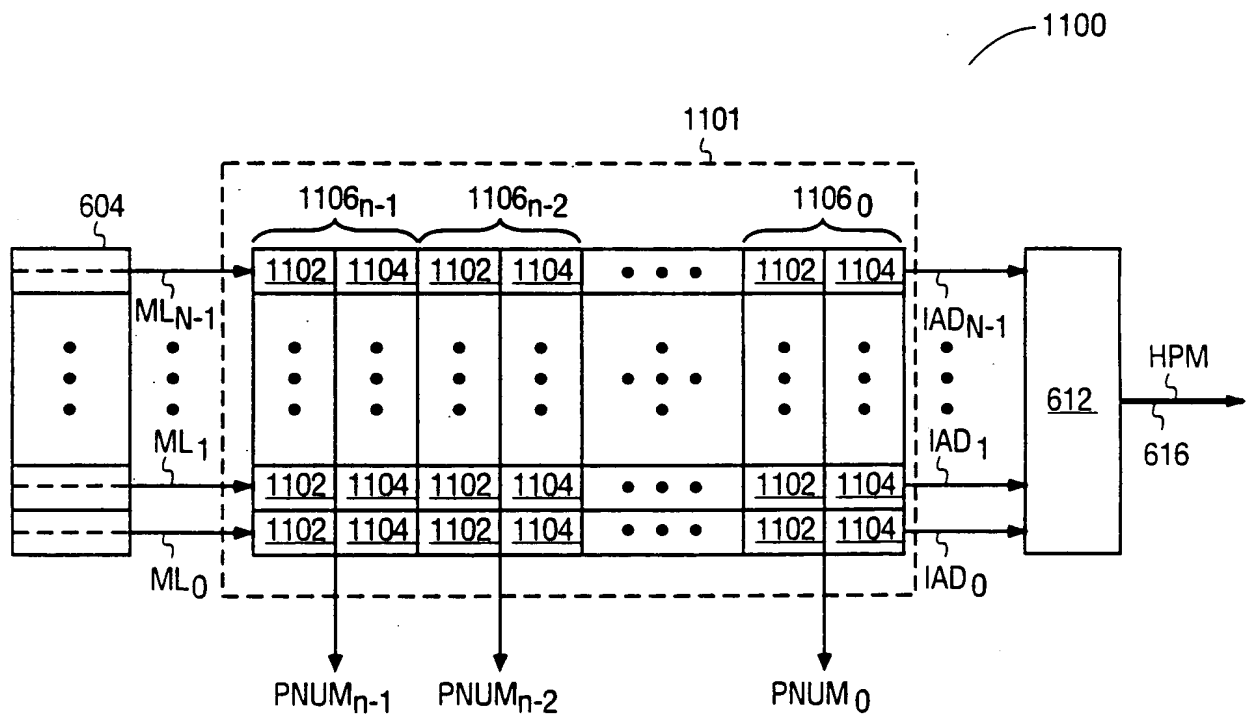


FIG. 11

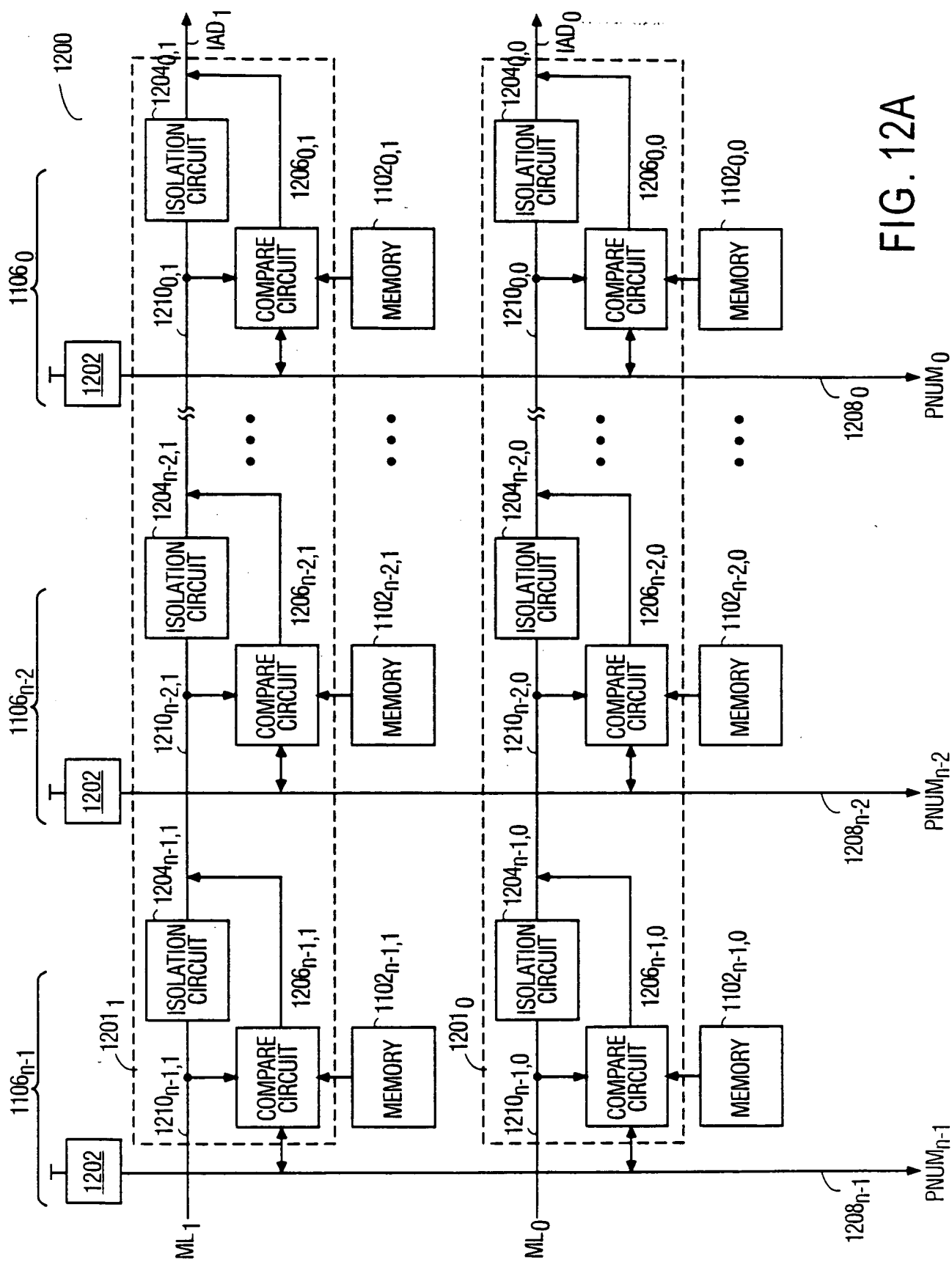


FIG. 12A

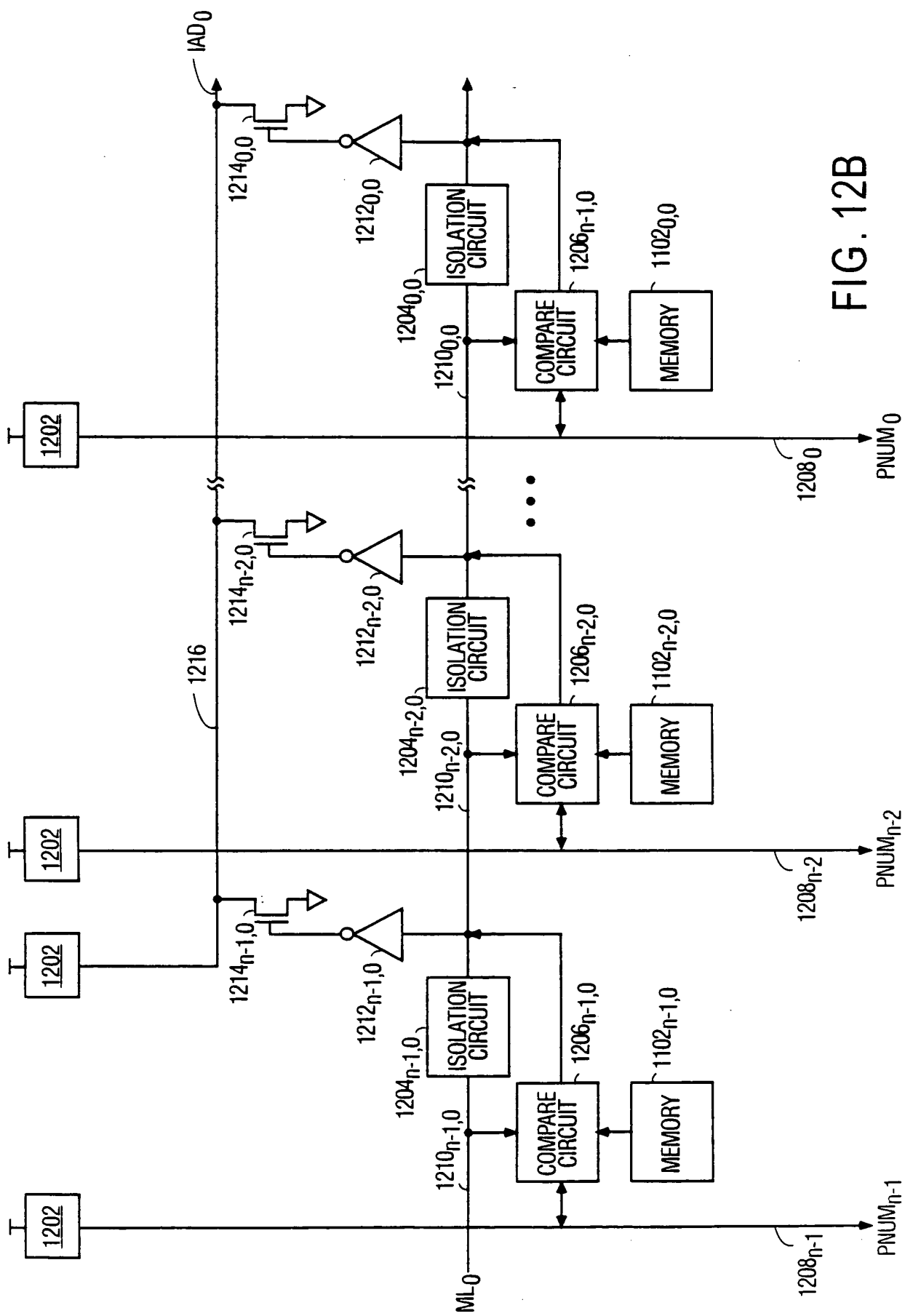


FIG. 12B

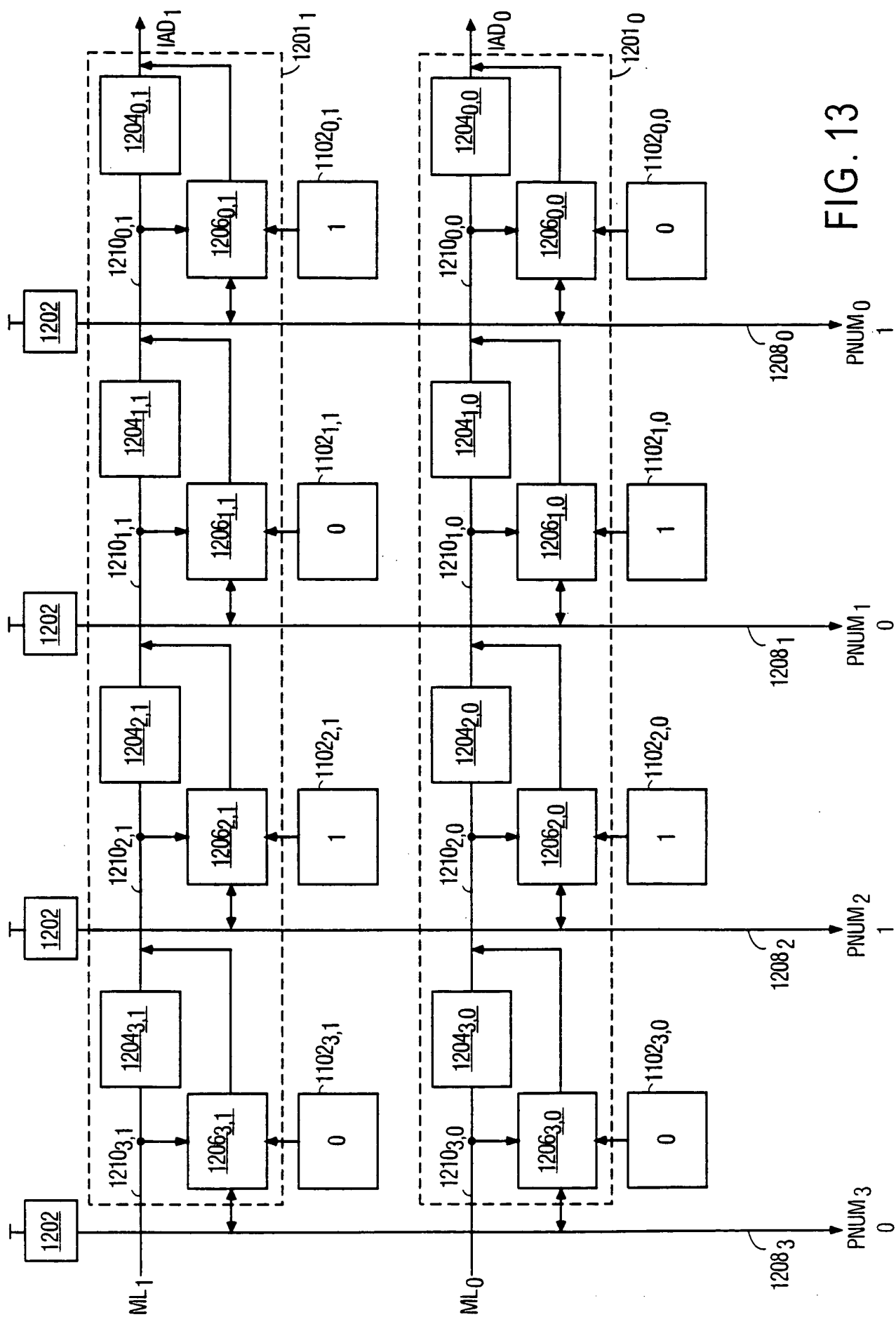


FIG. 13

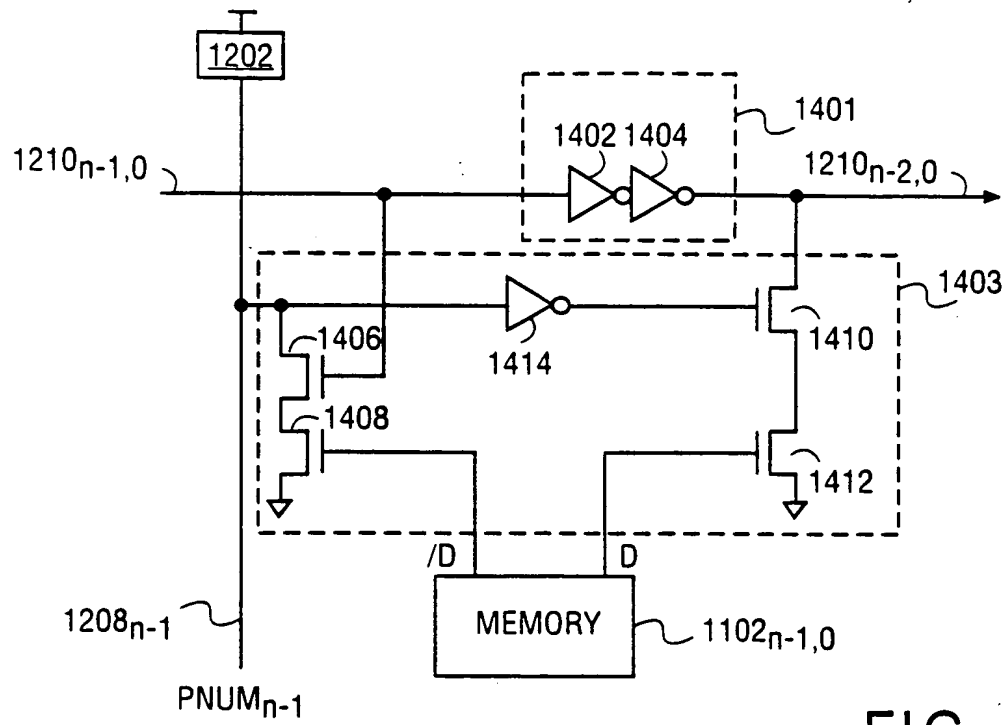


FIG. 14

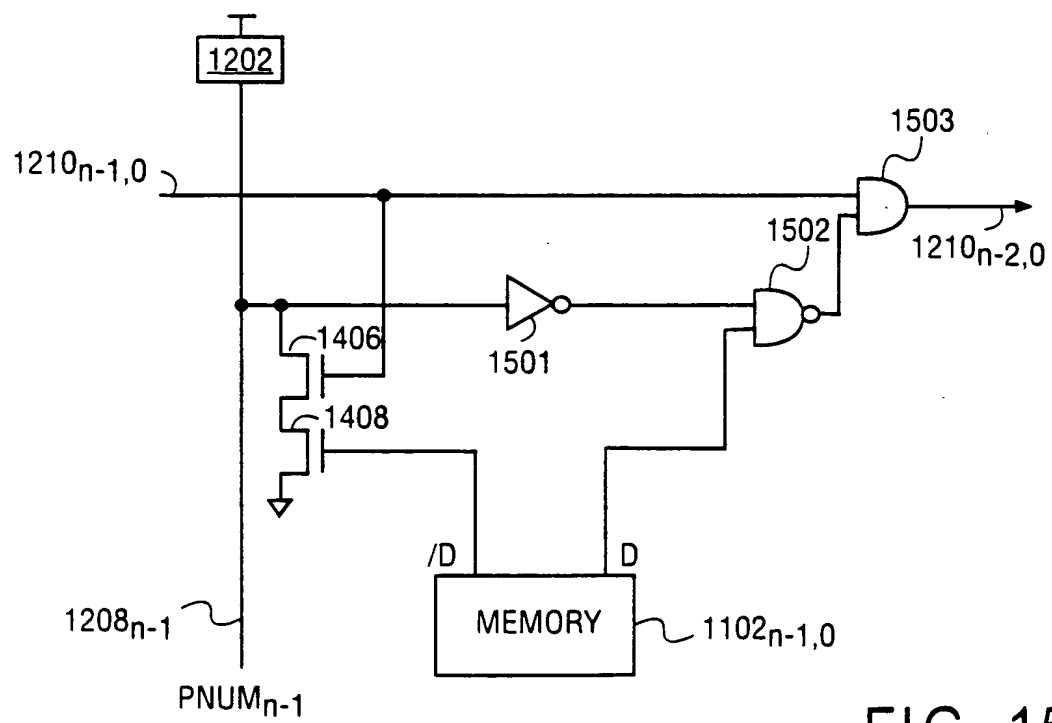


FIG. 15

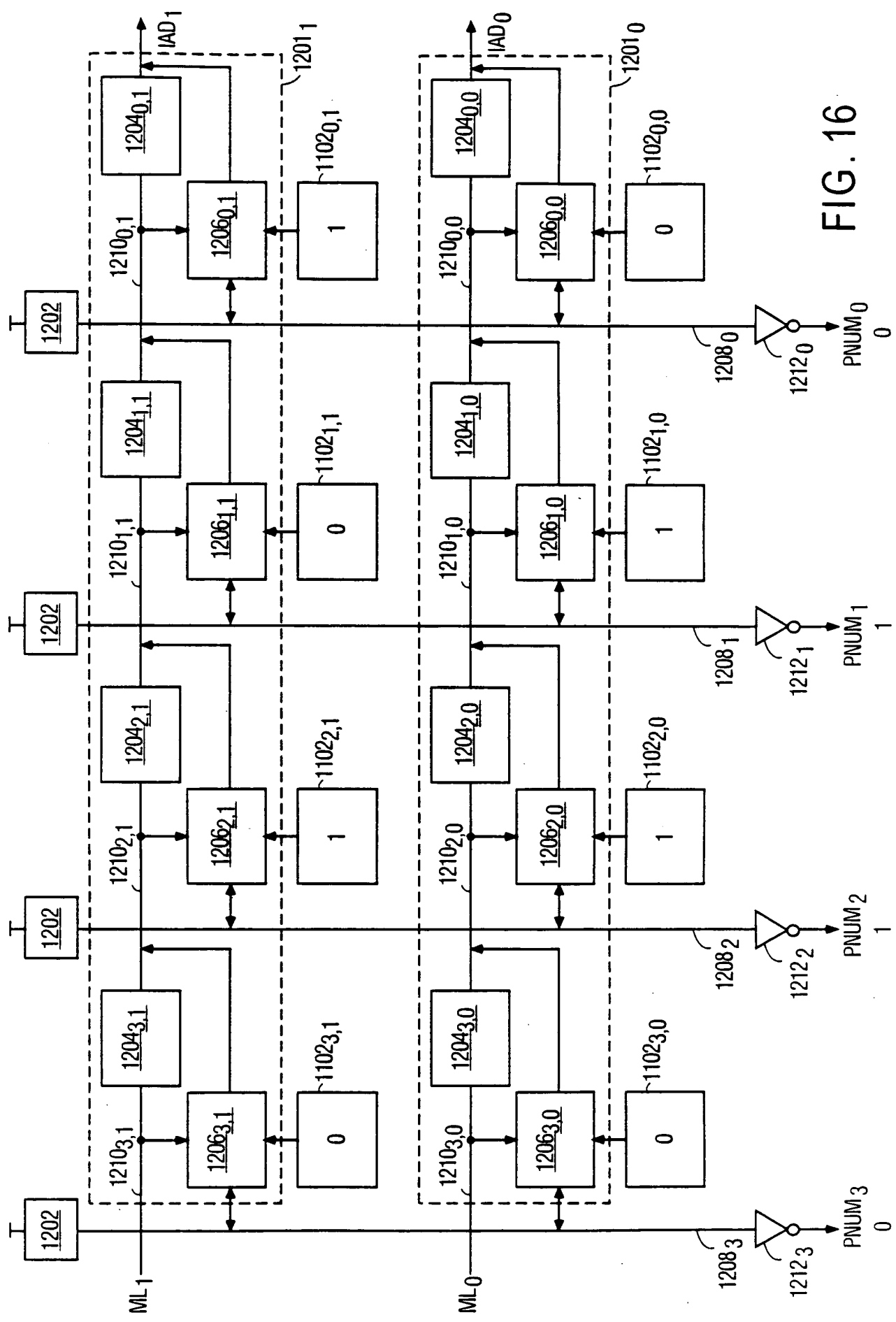


FIG. 16

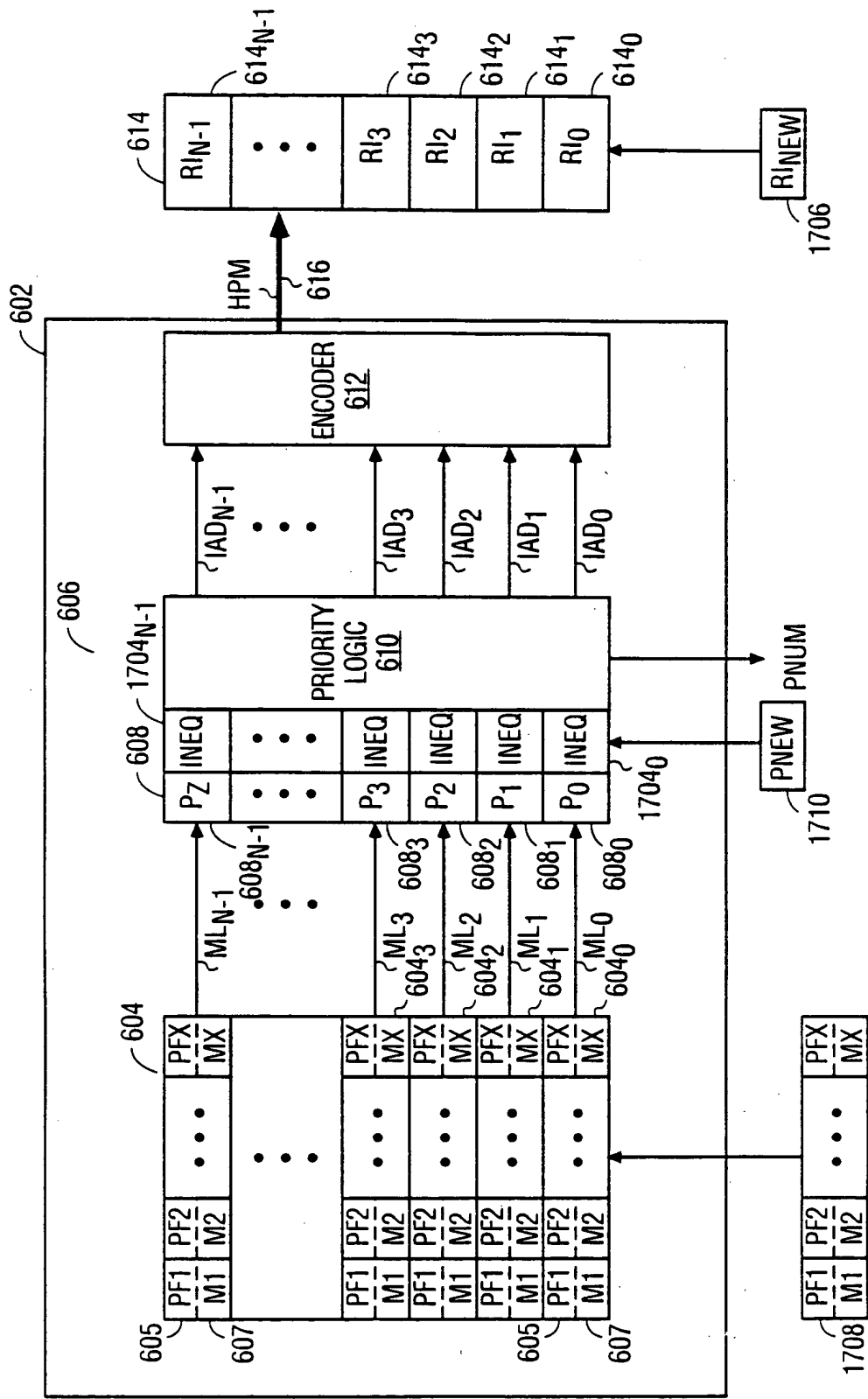


FIG. 17

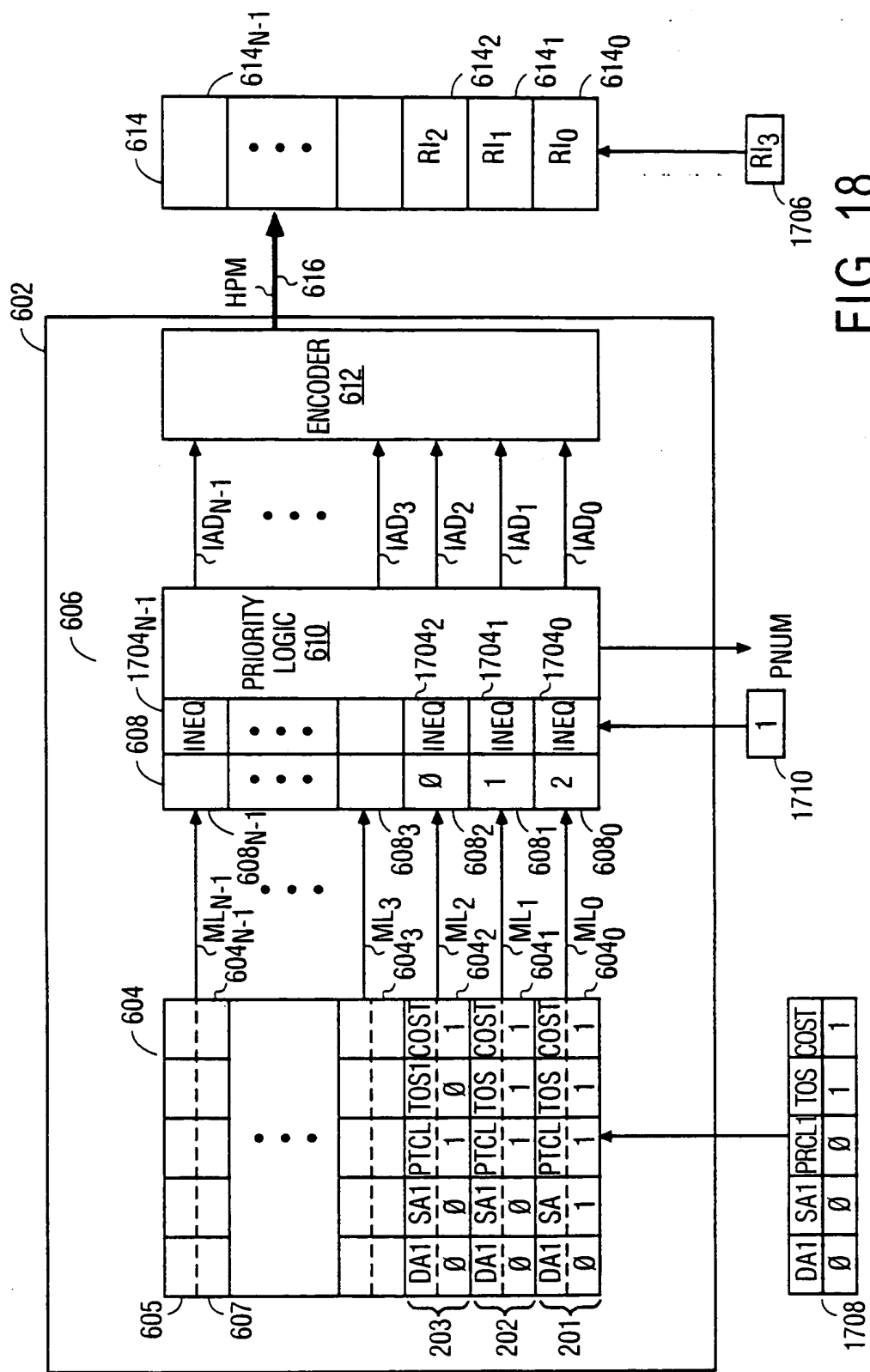


FIG. 18

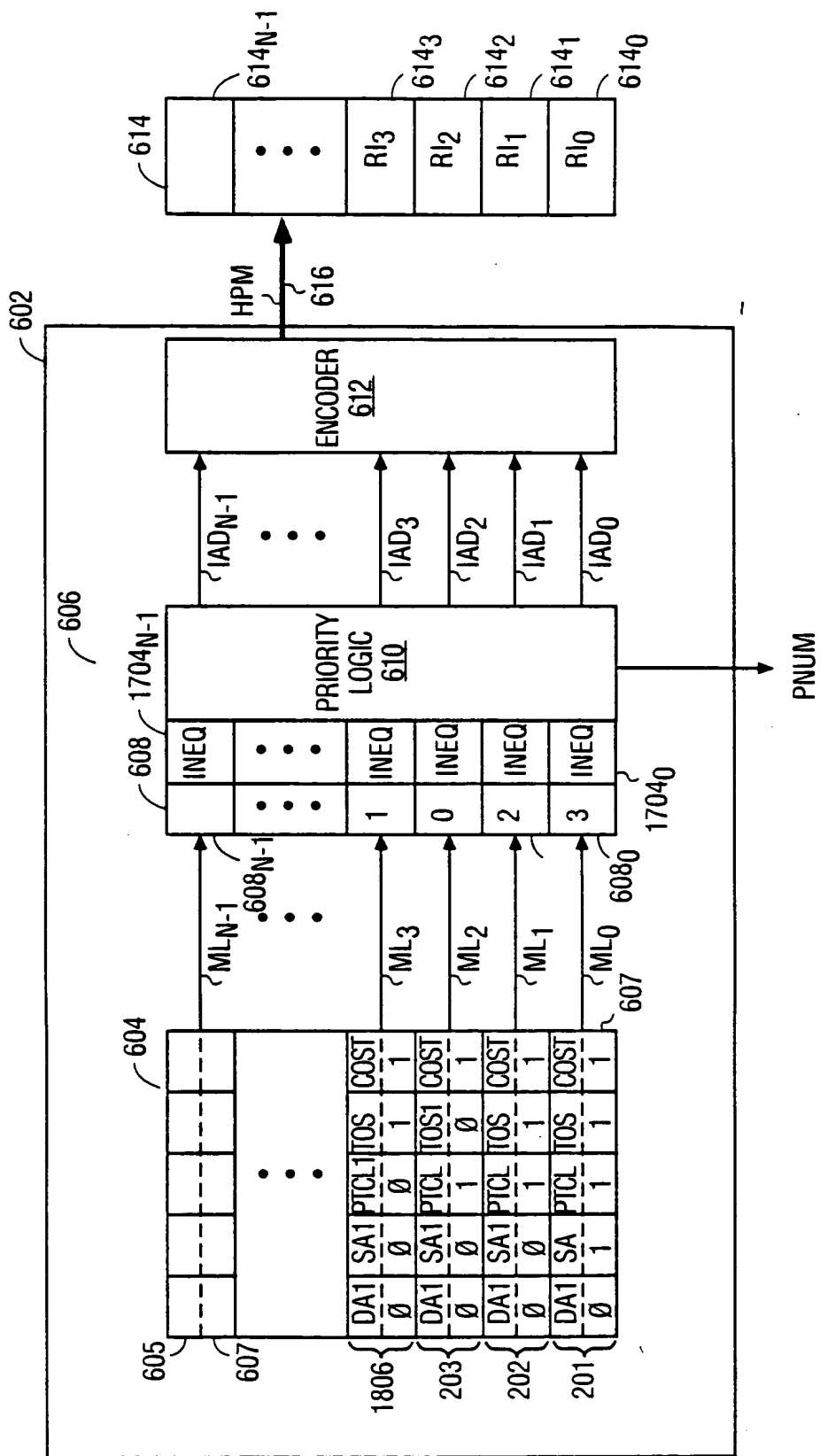


FIG. 19

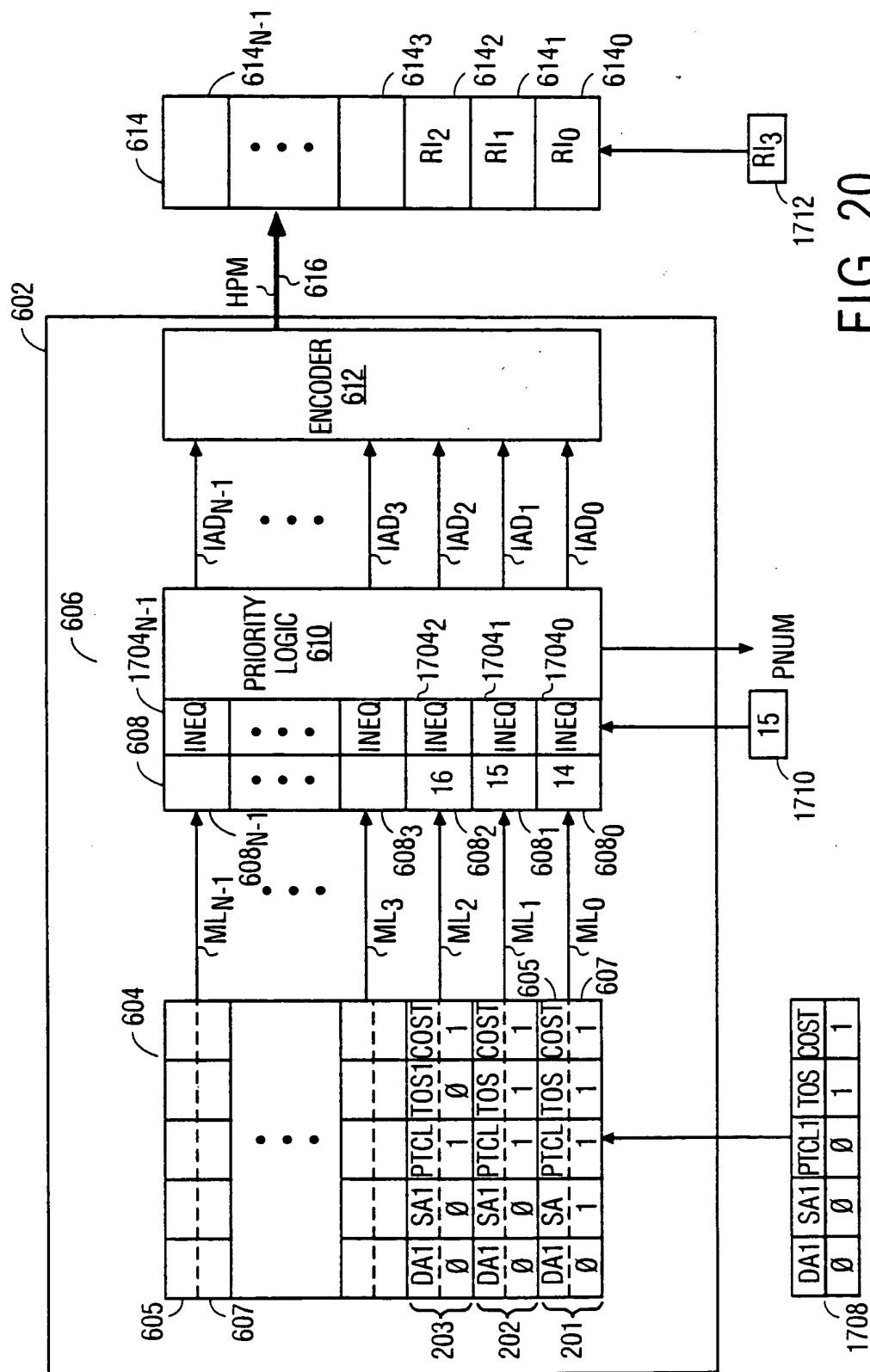


FIG. 20

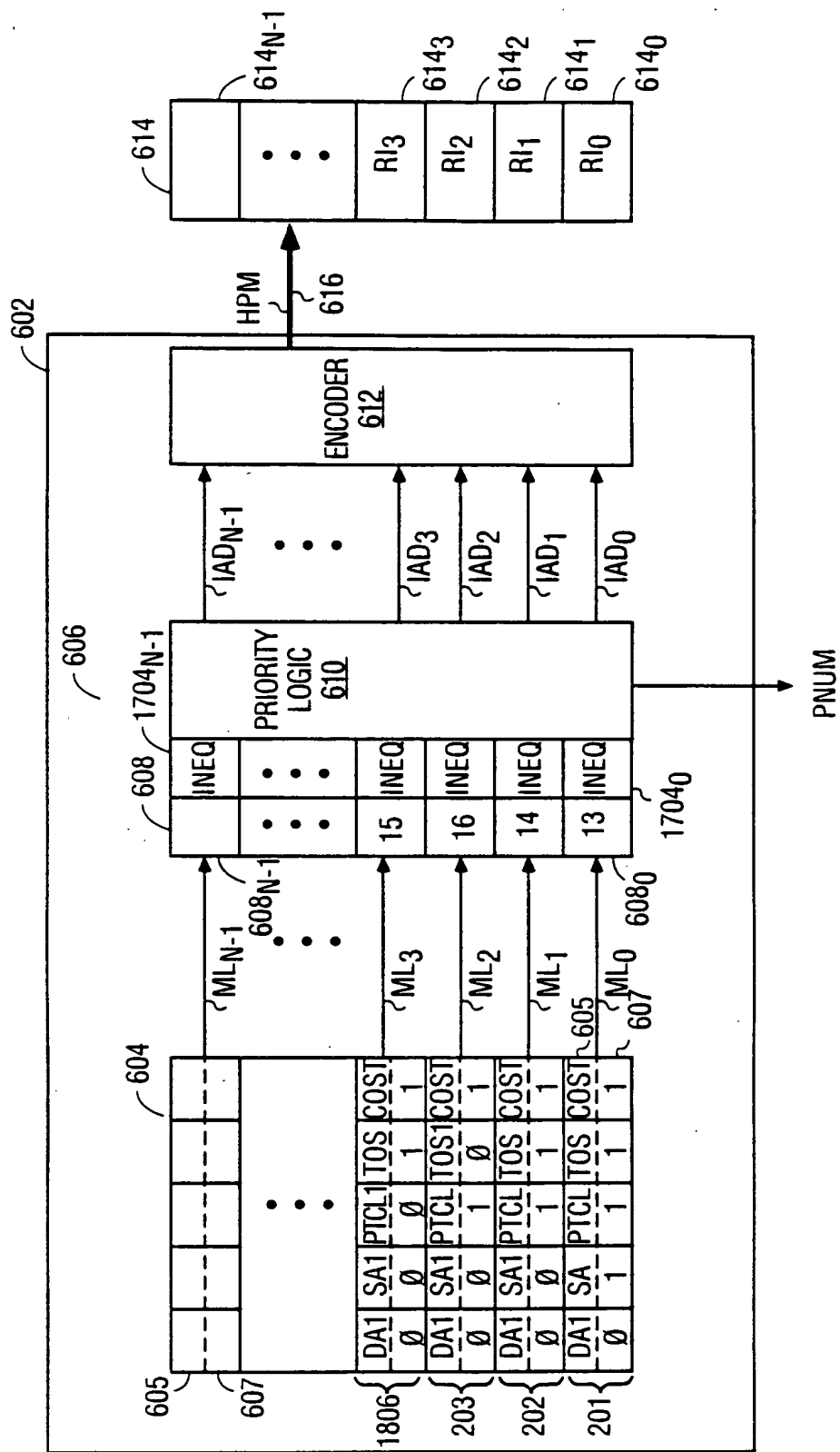


FIG. 21

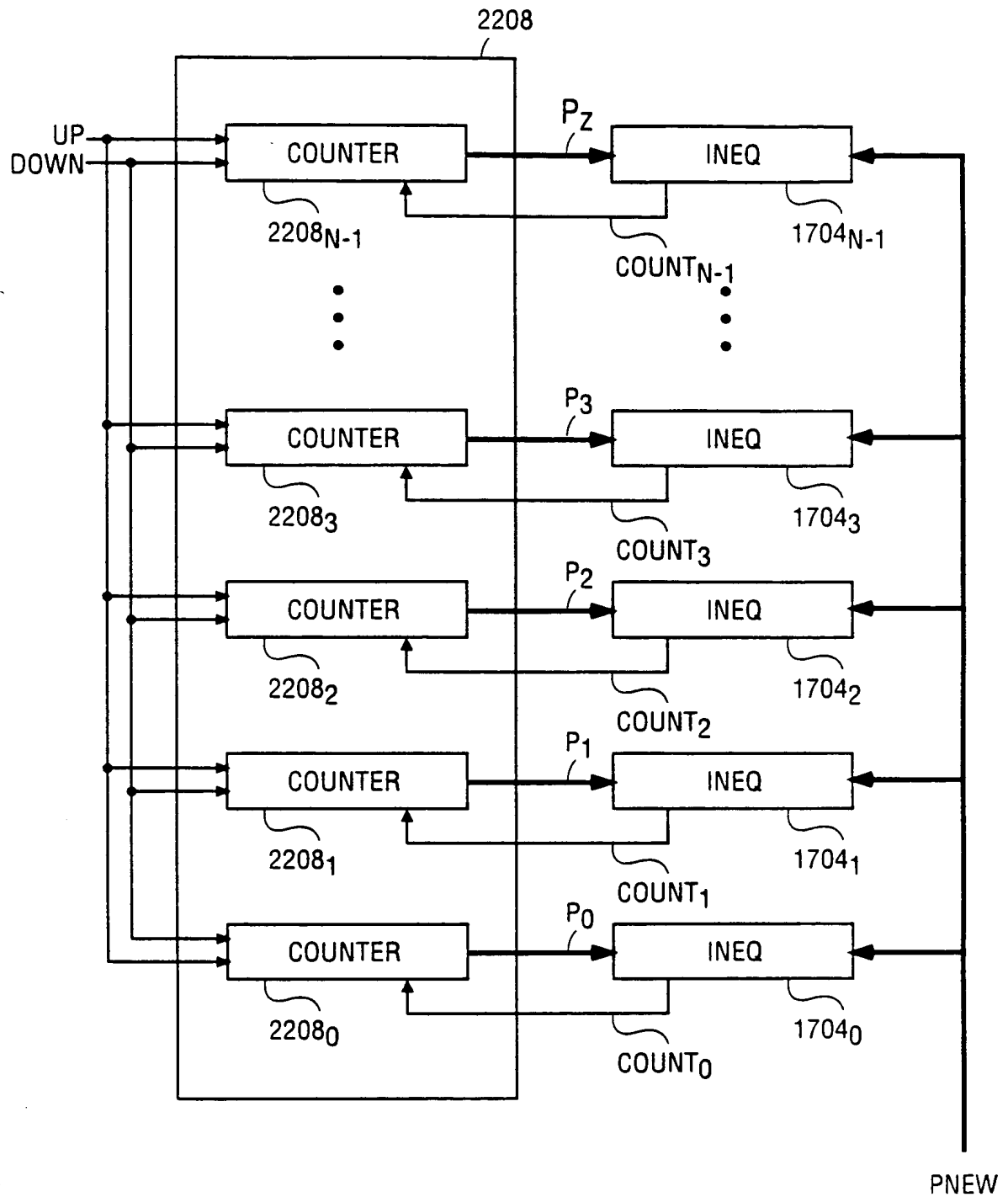
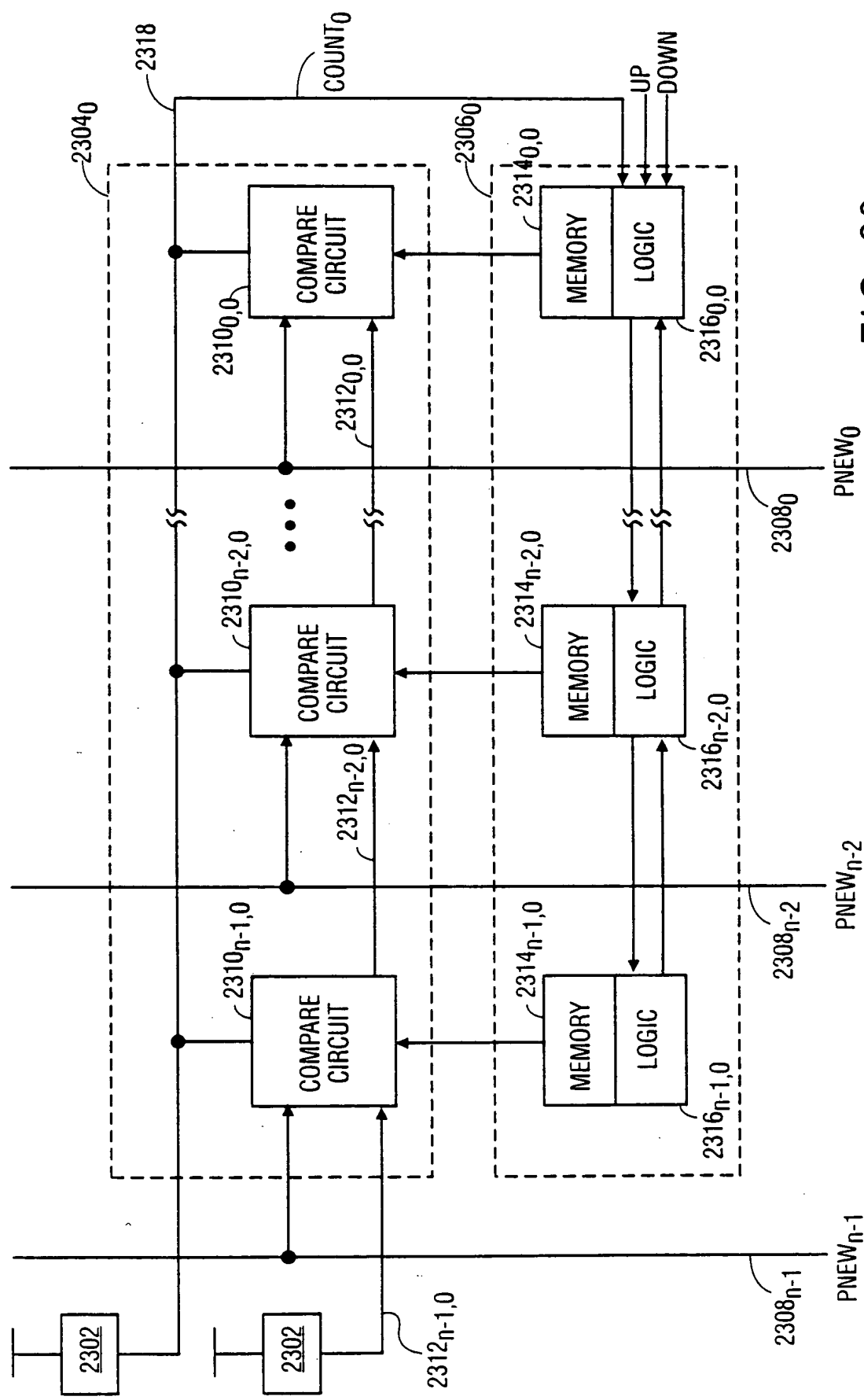


FIG. 22



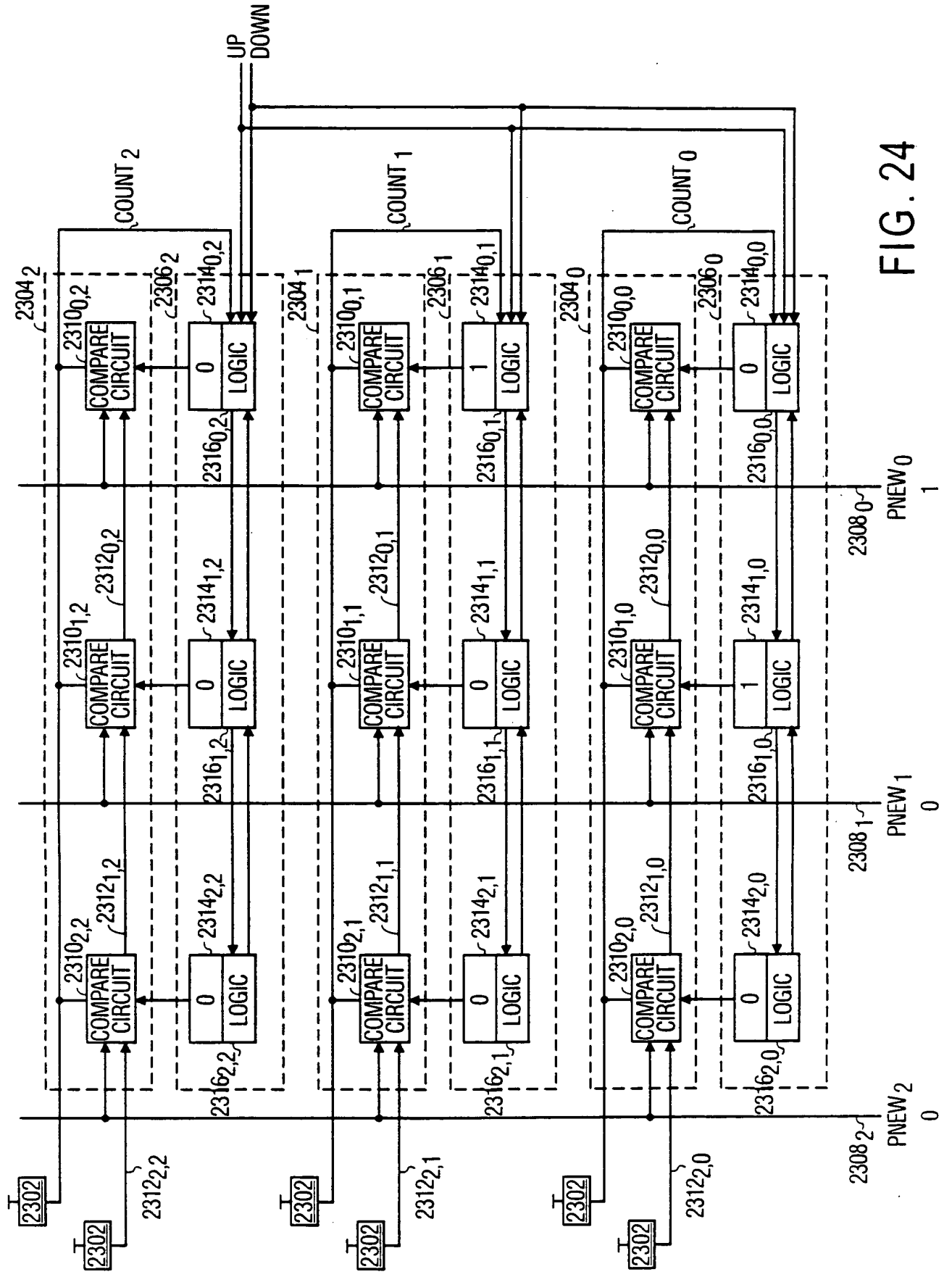


FIG. 24

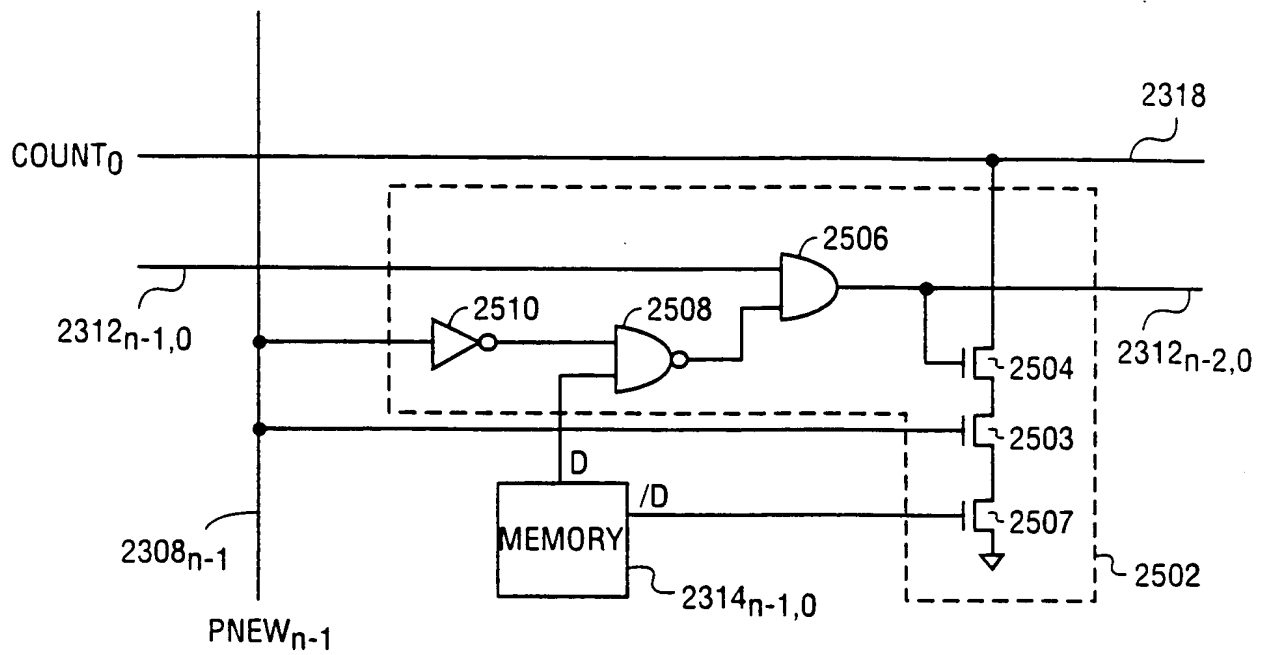


FIG. 25A

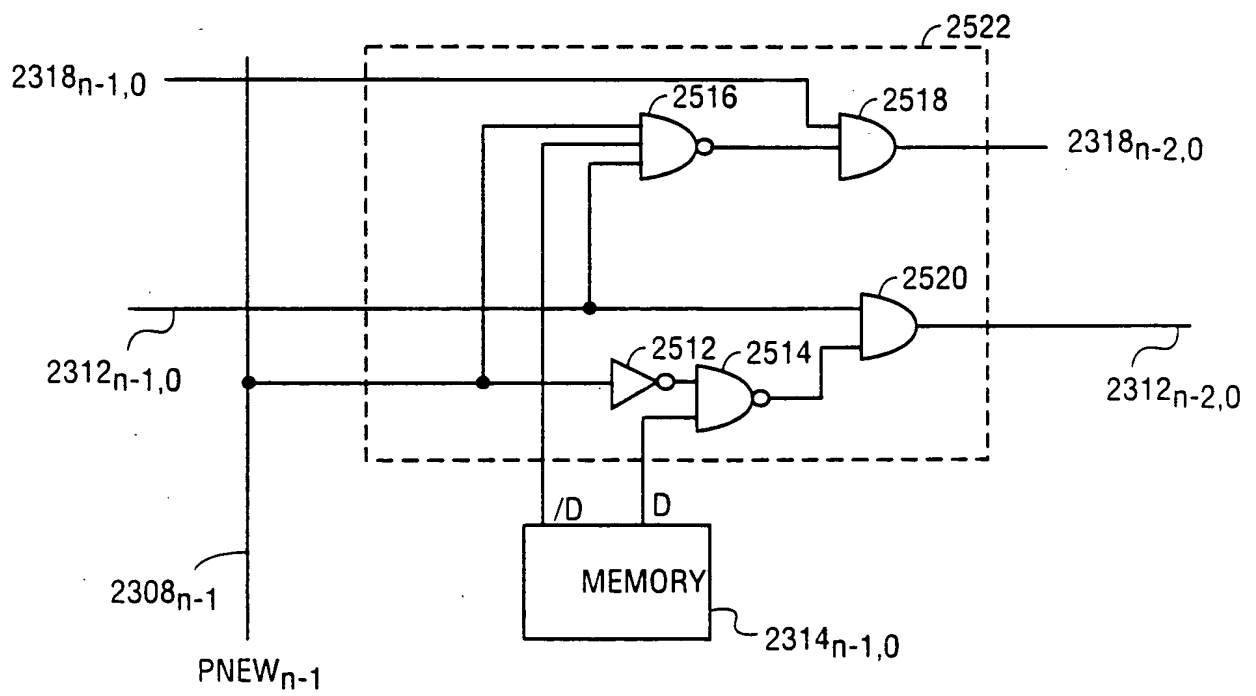


FIG. 25B

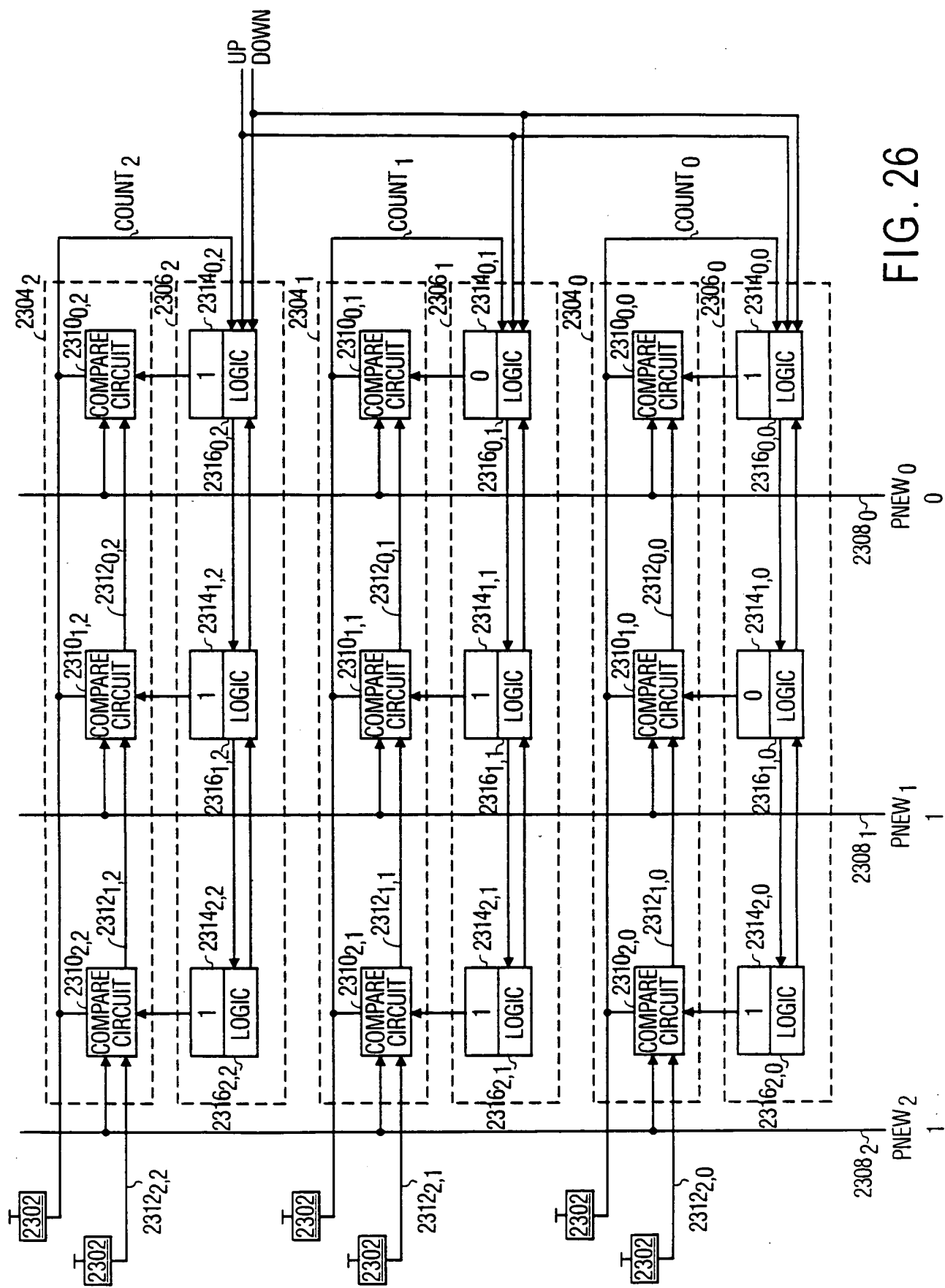


FIG. 26

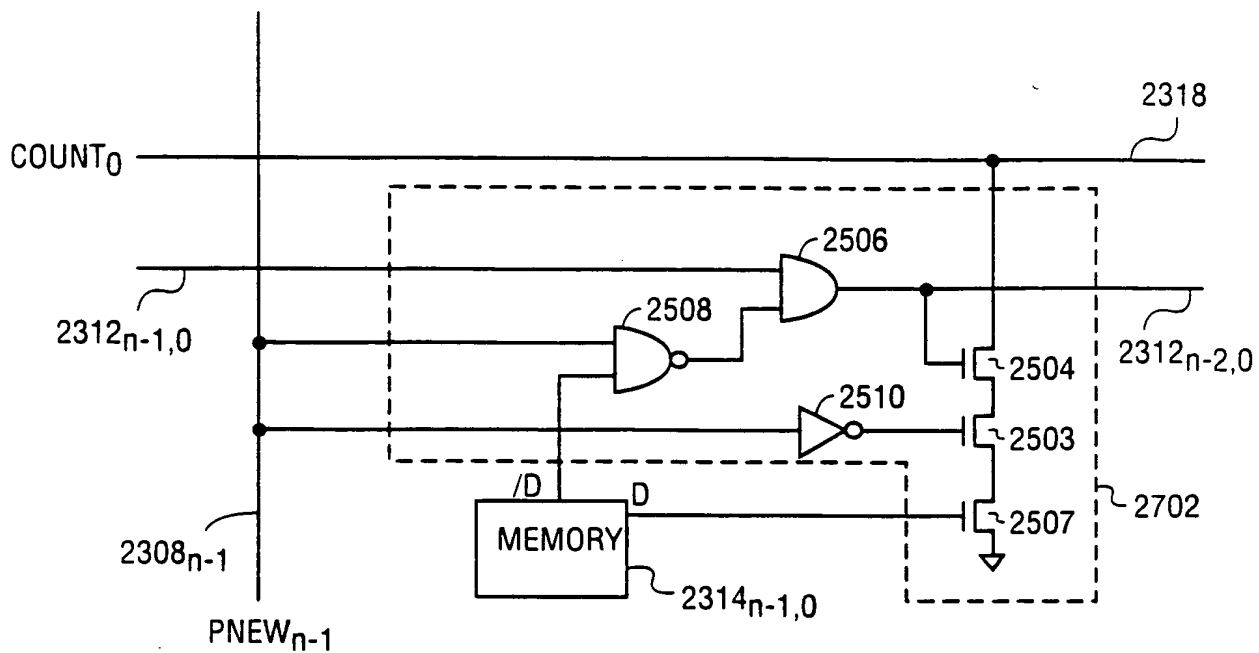


FIG. 27A

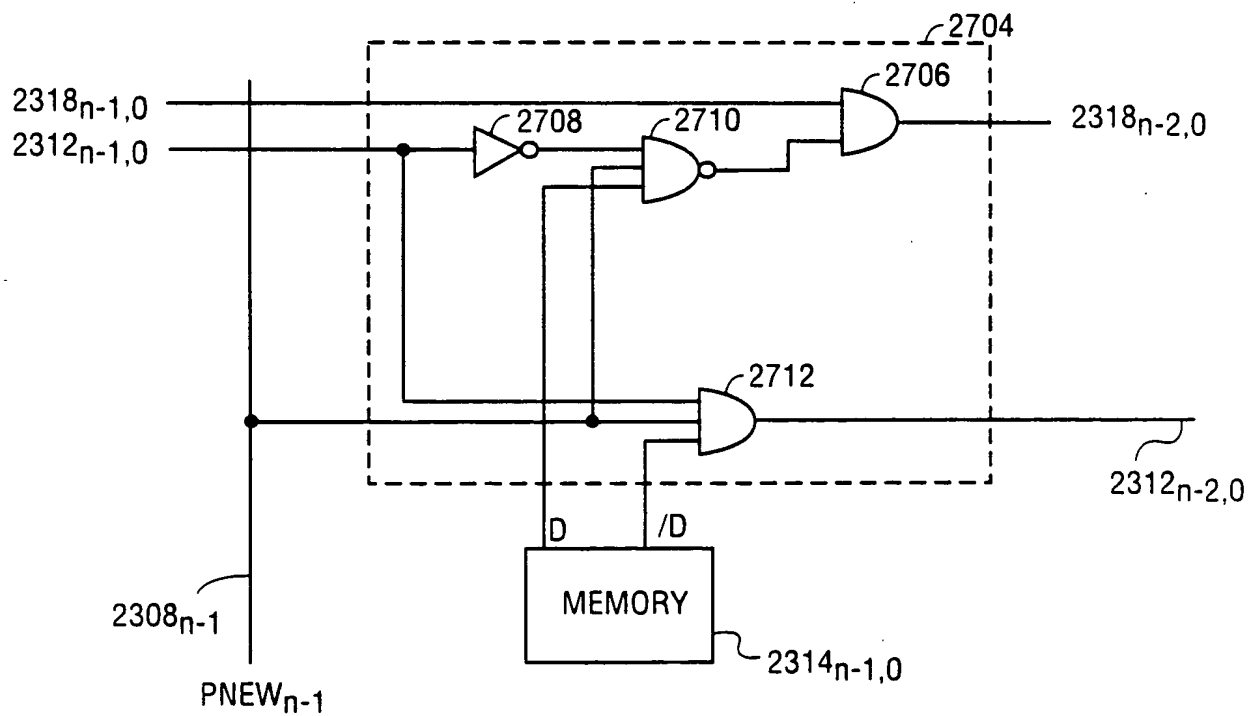


FIG. 27B

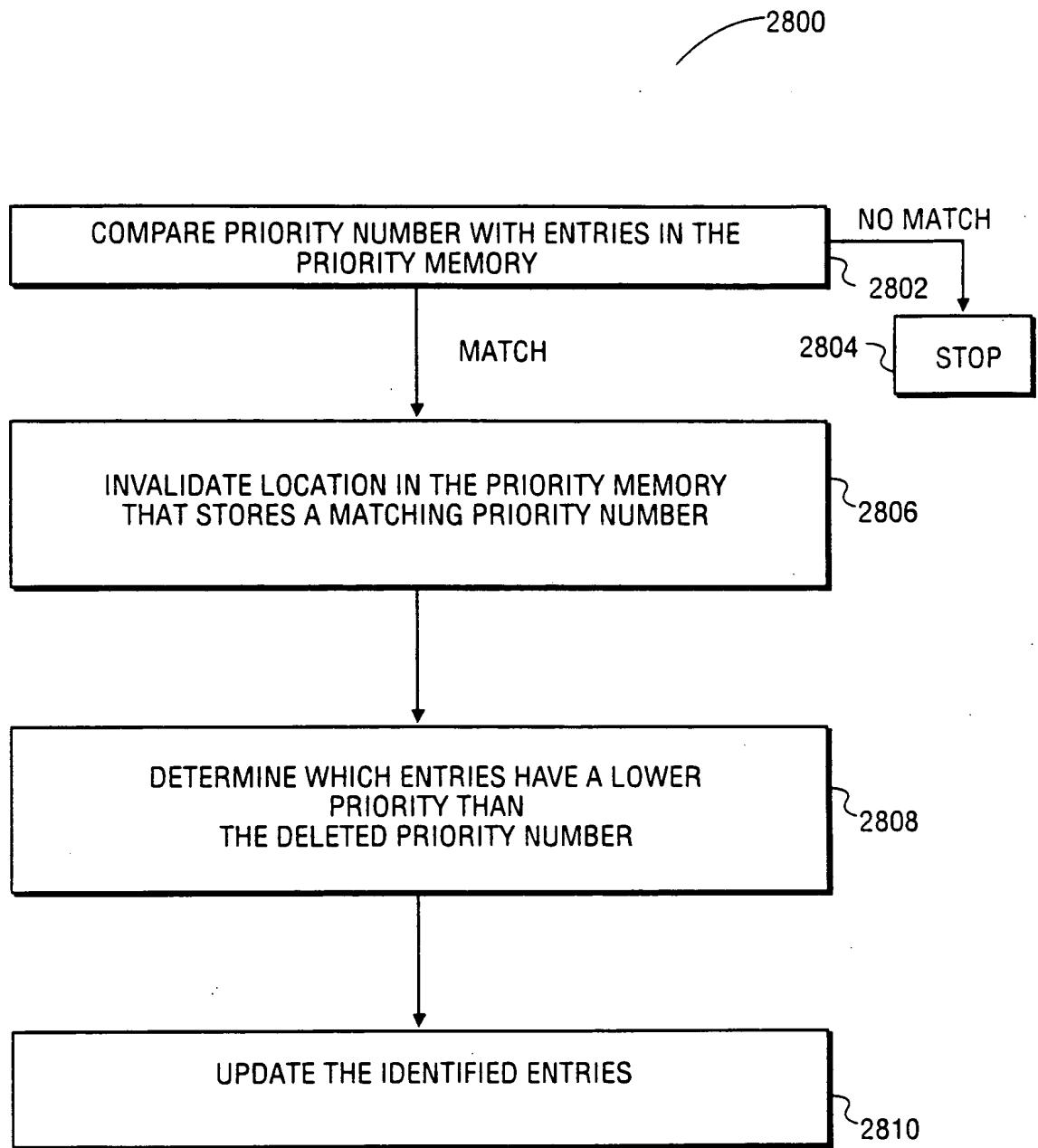


FIG. 28

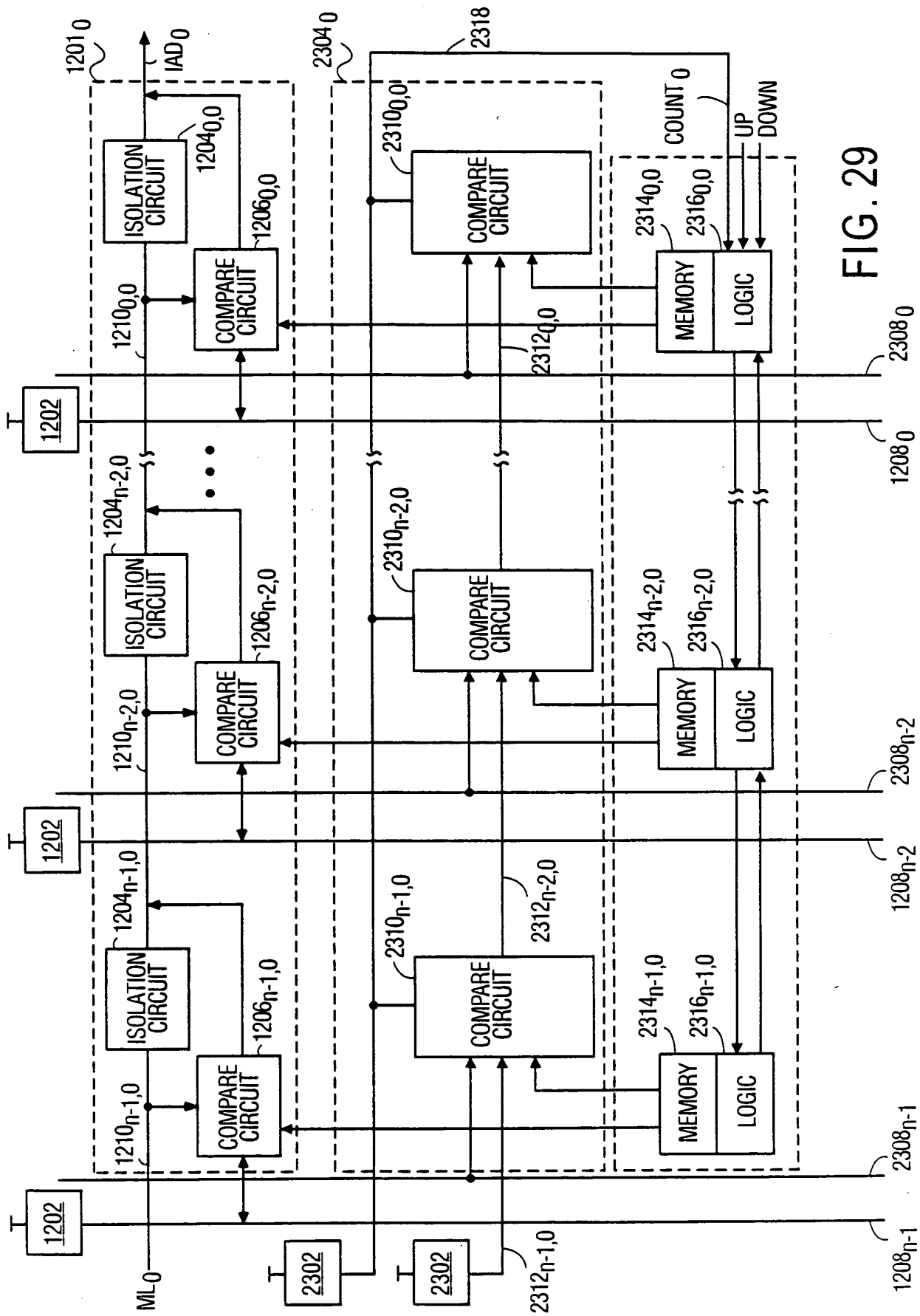


FIG. 29

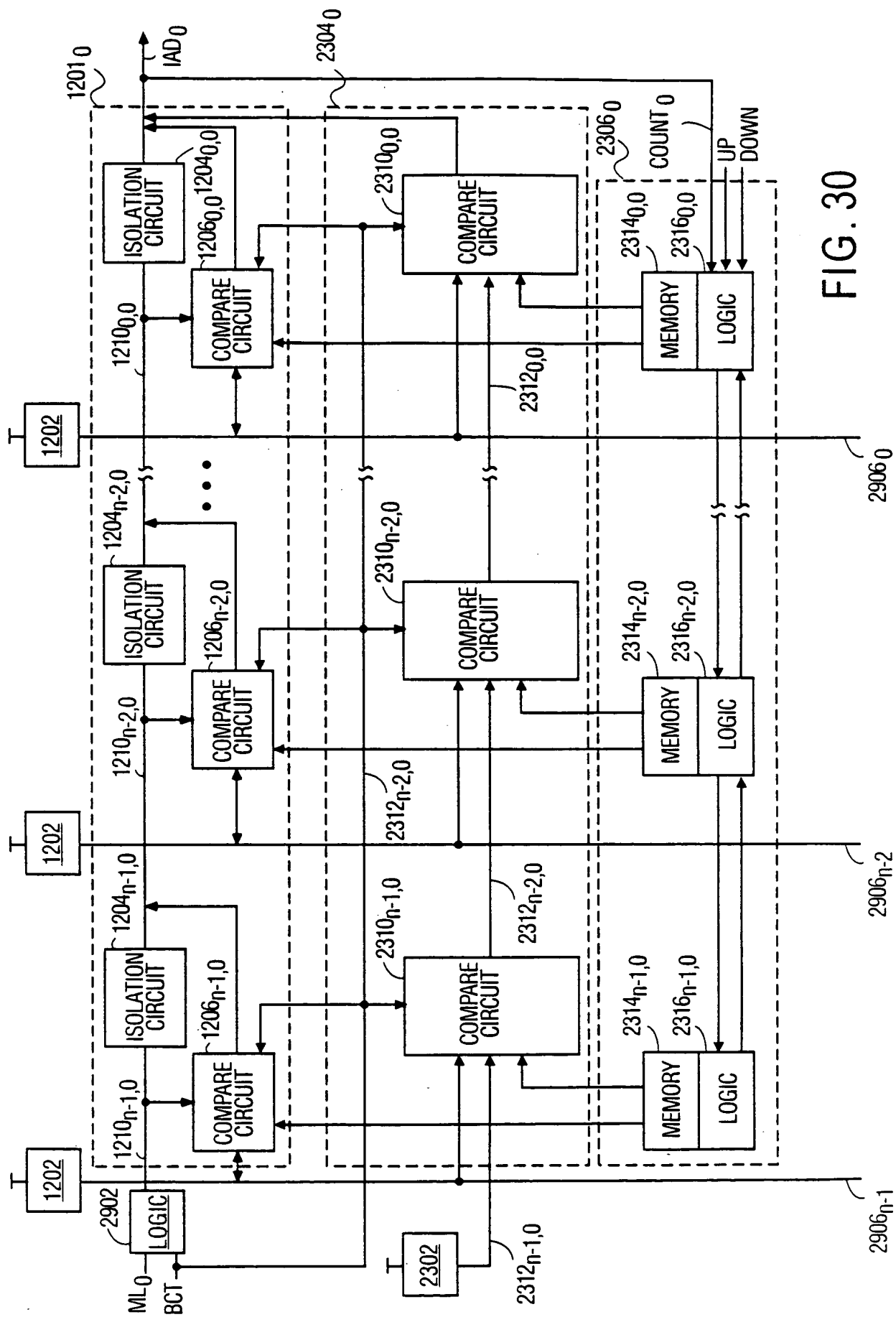
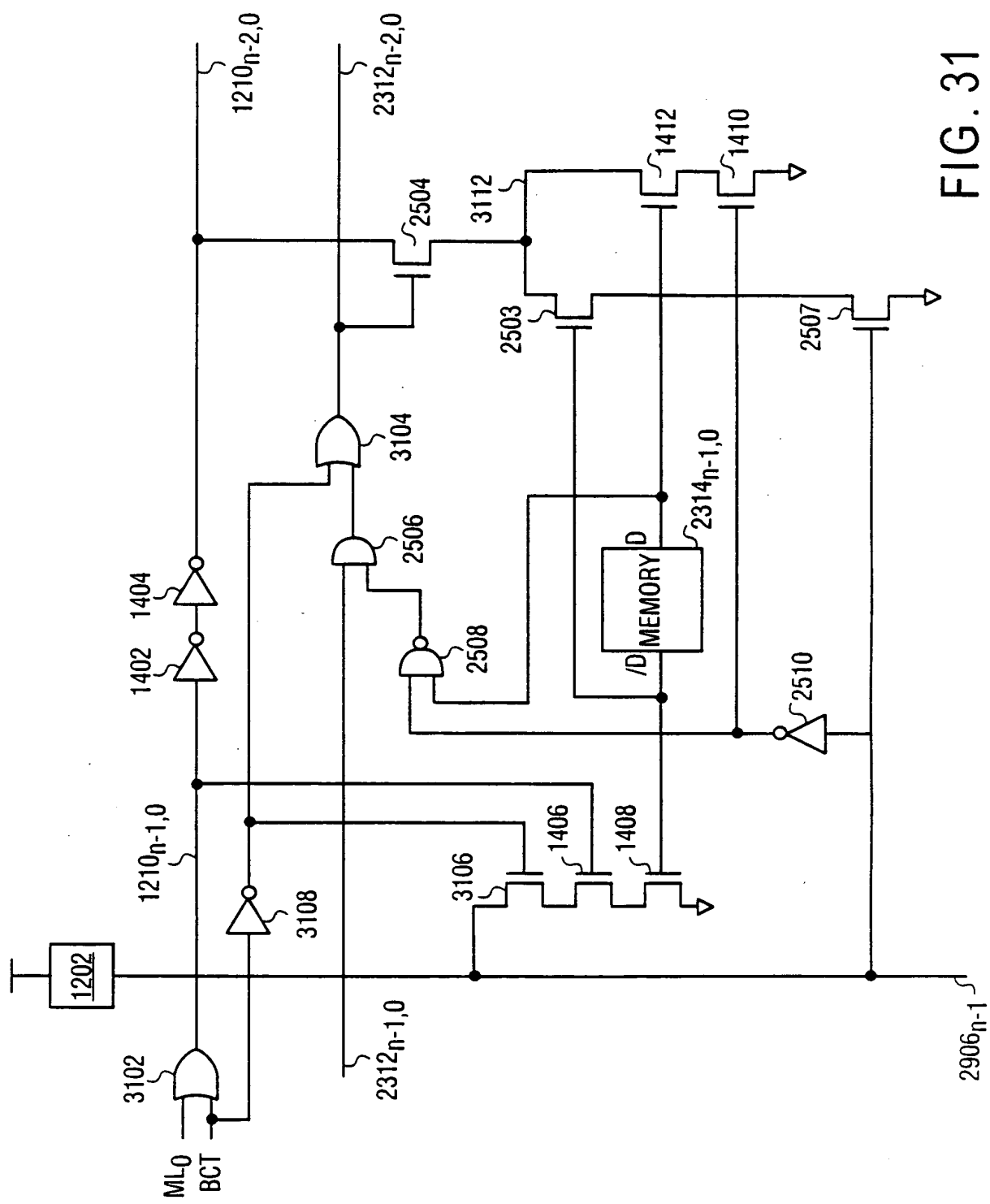


FIG. 30



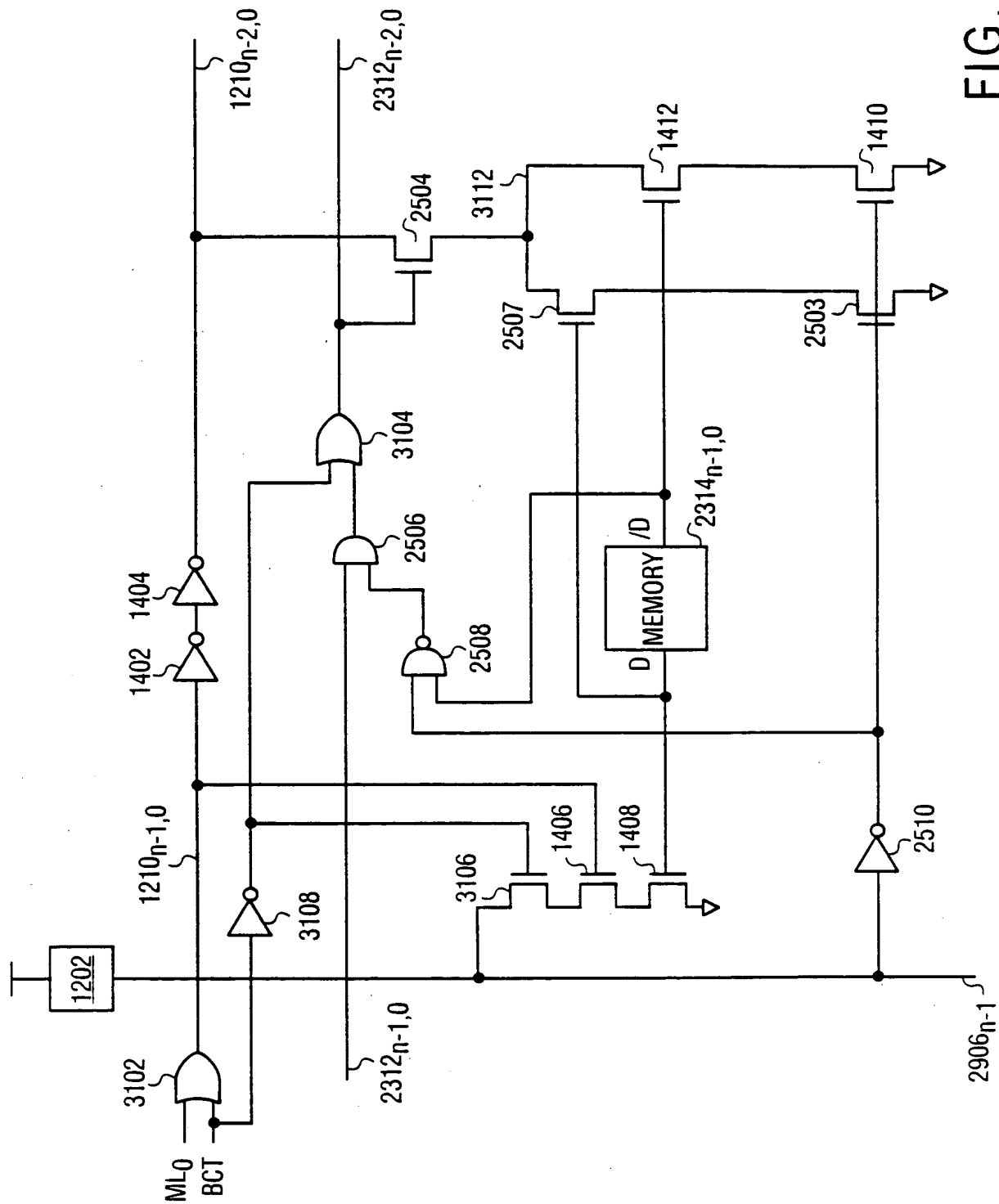
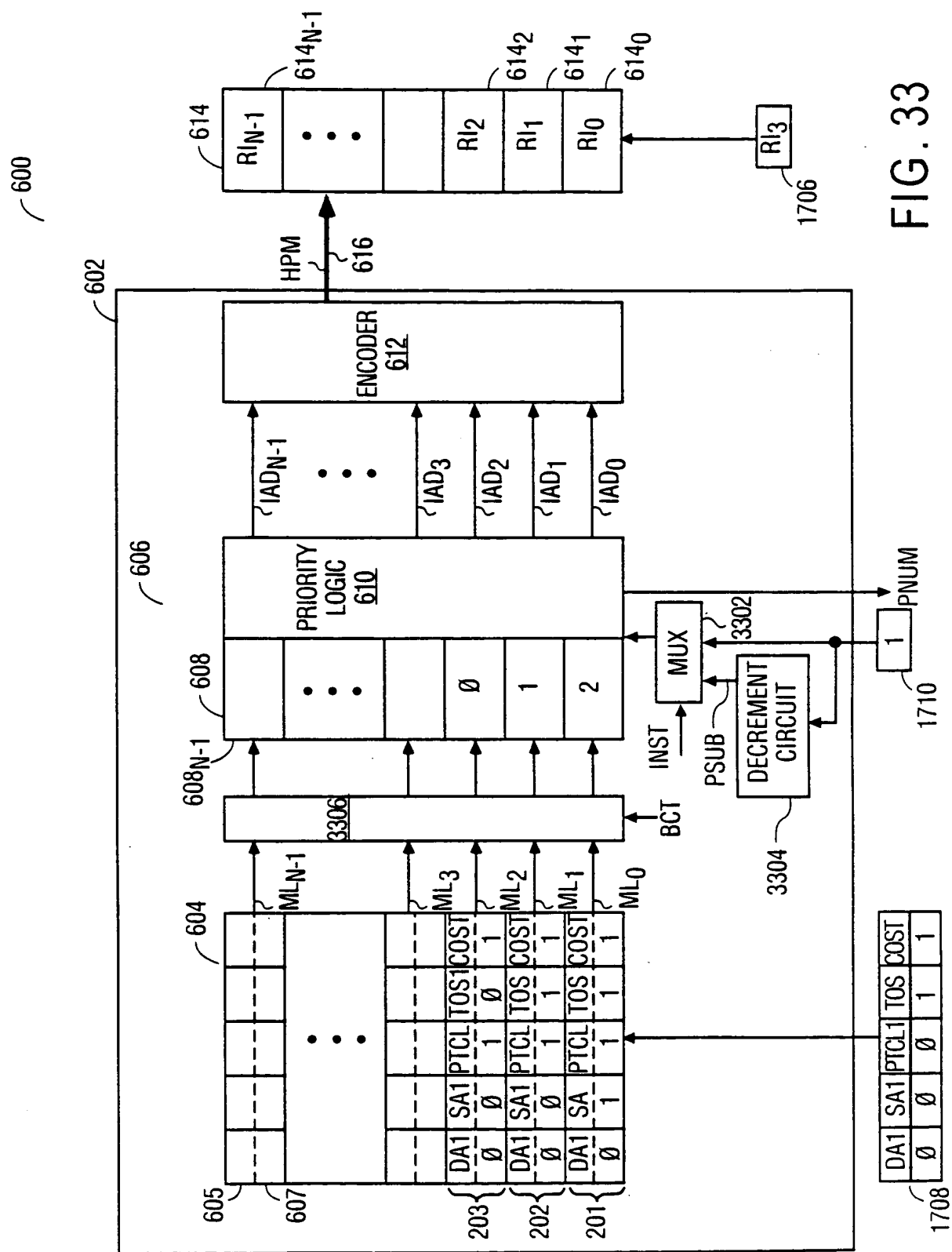


FIG. 32



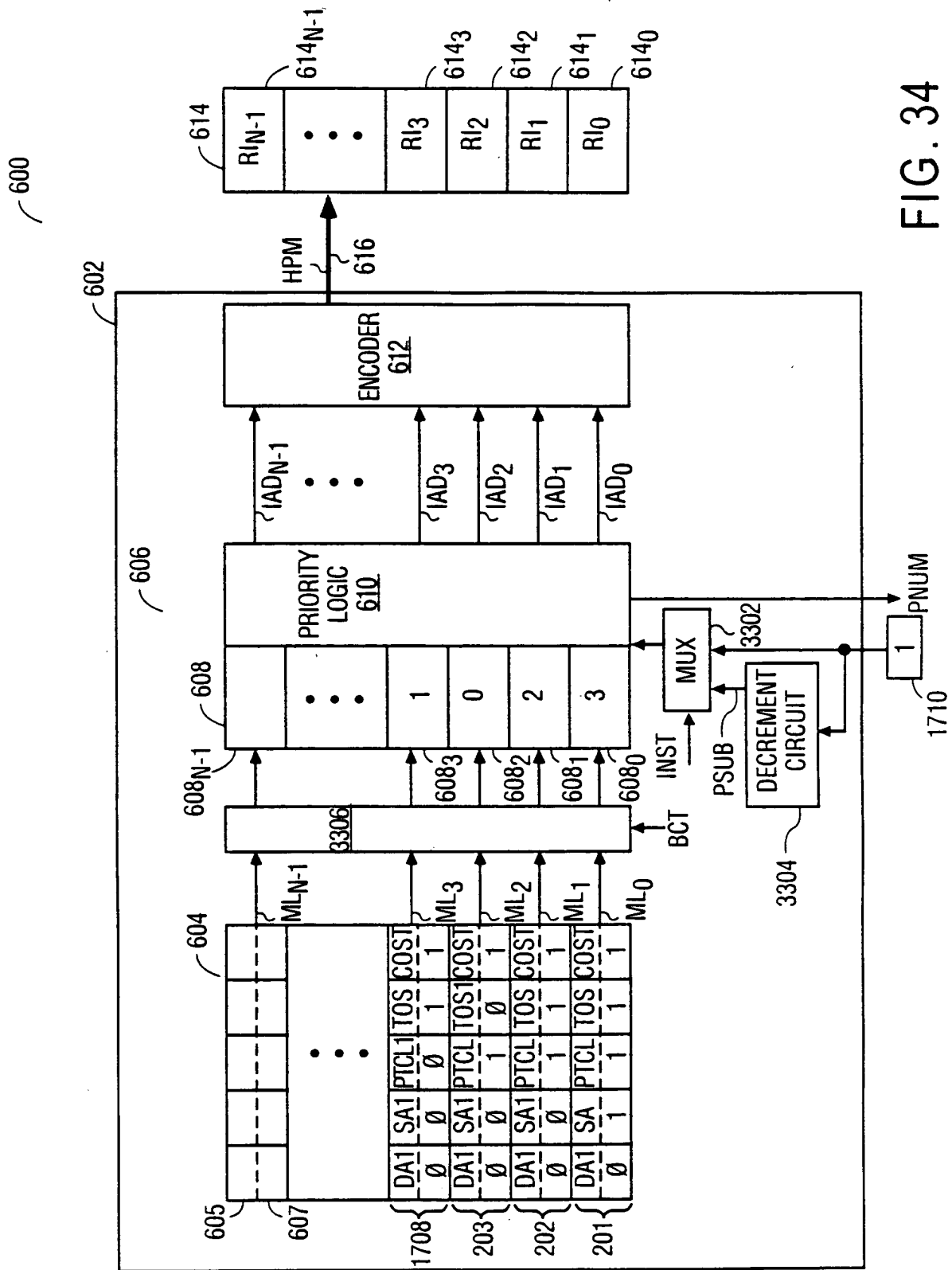


FIG. 34

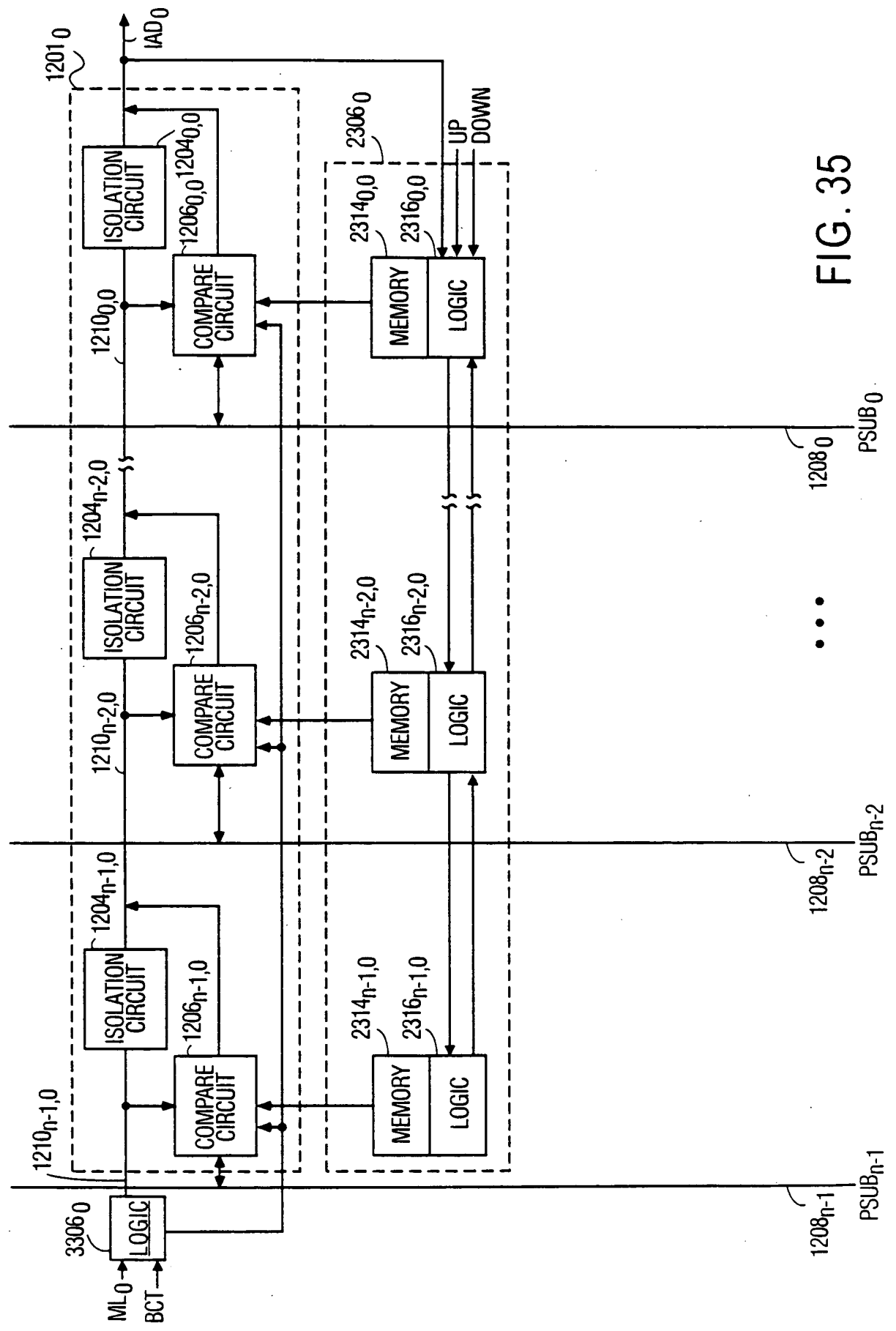
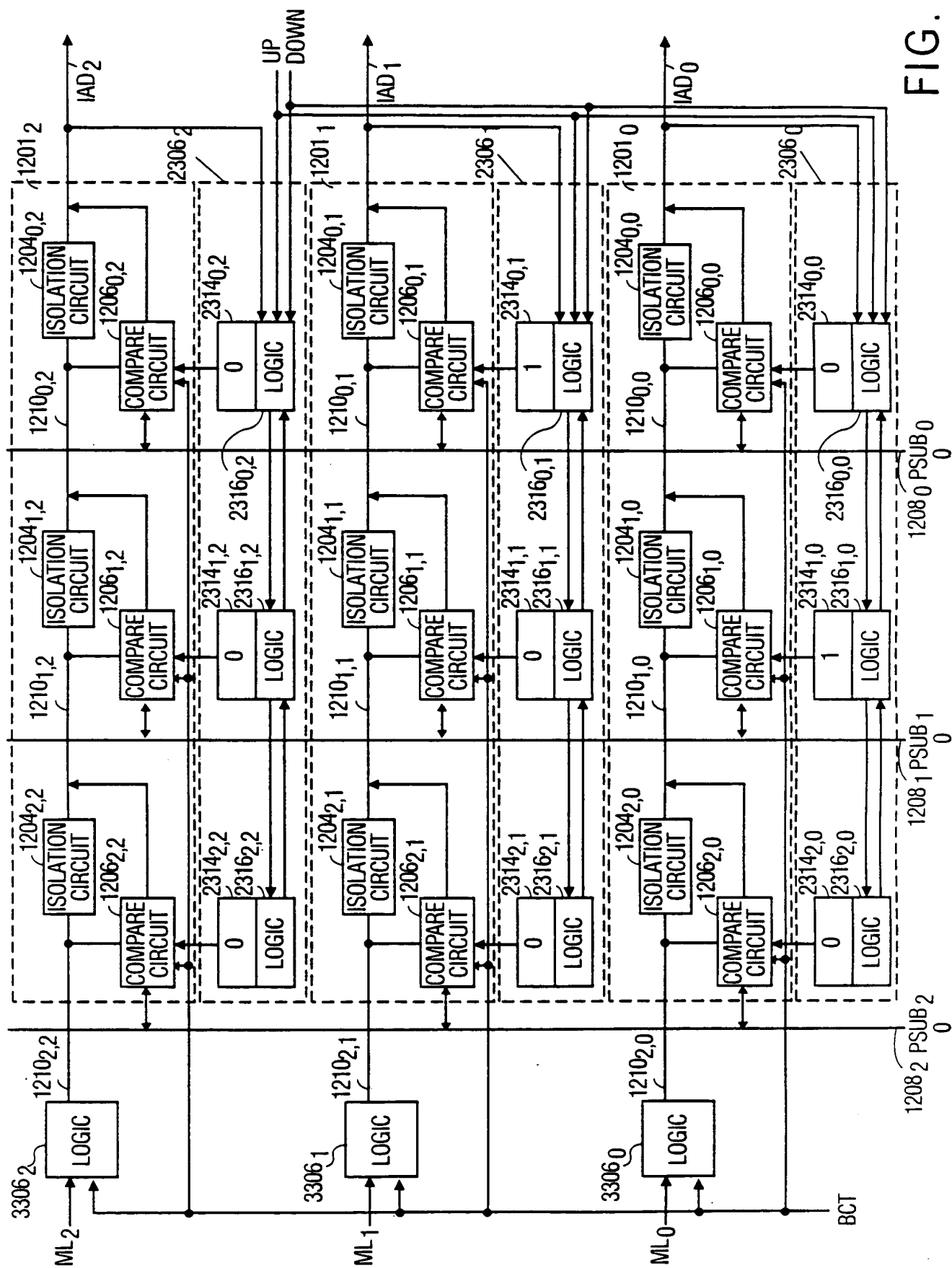


FIG. 35



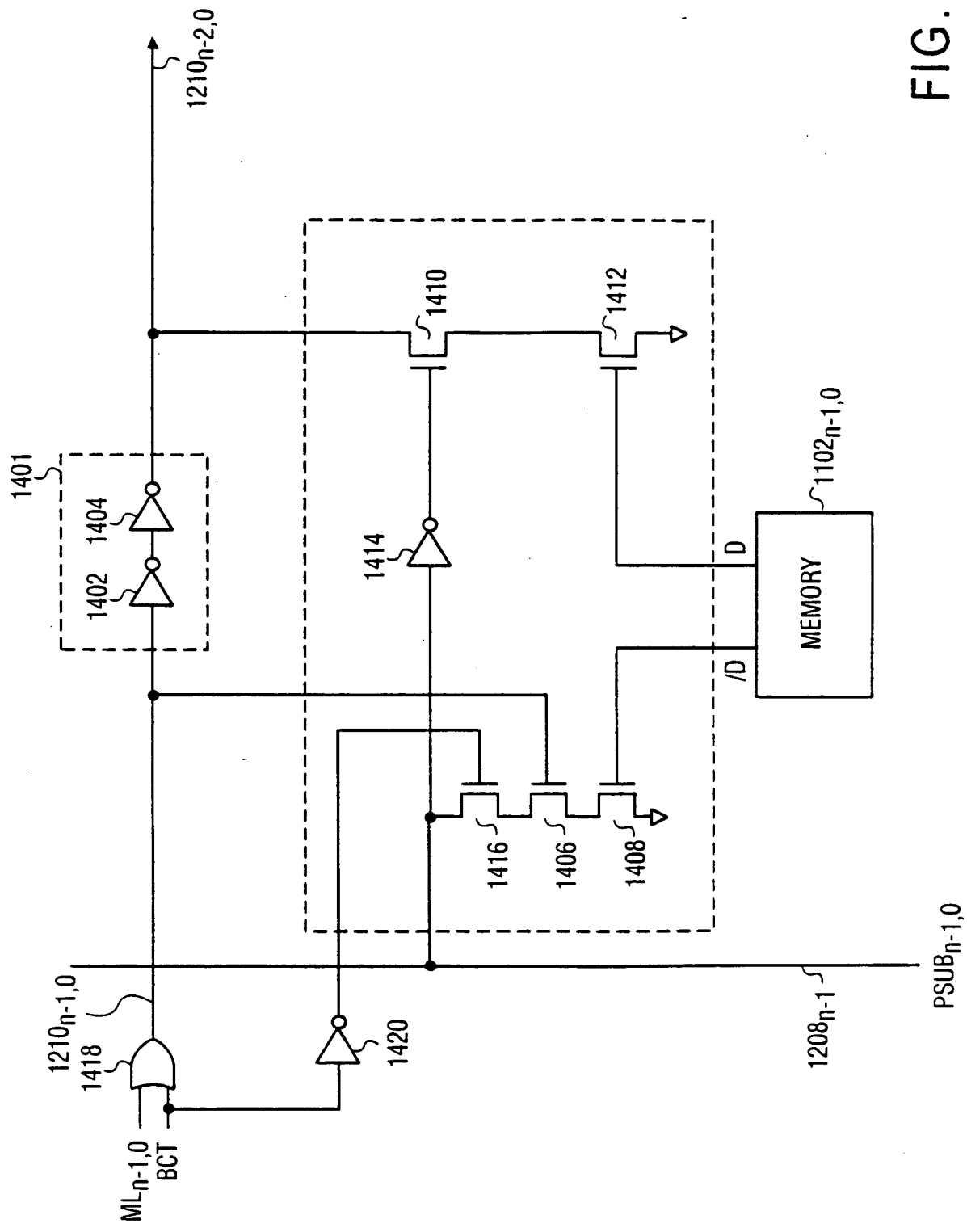
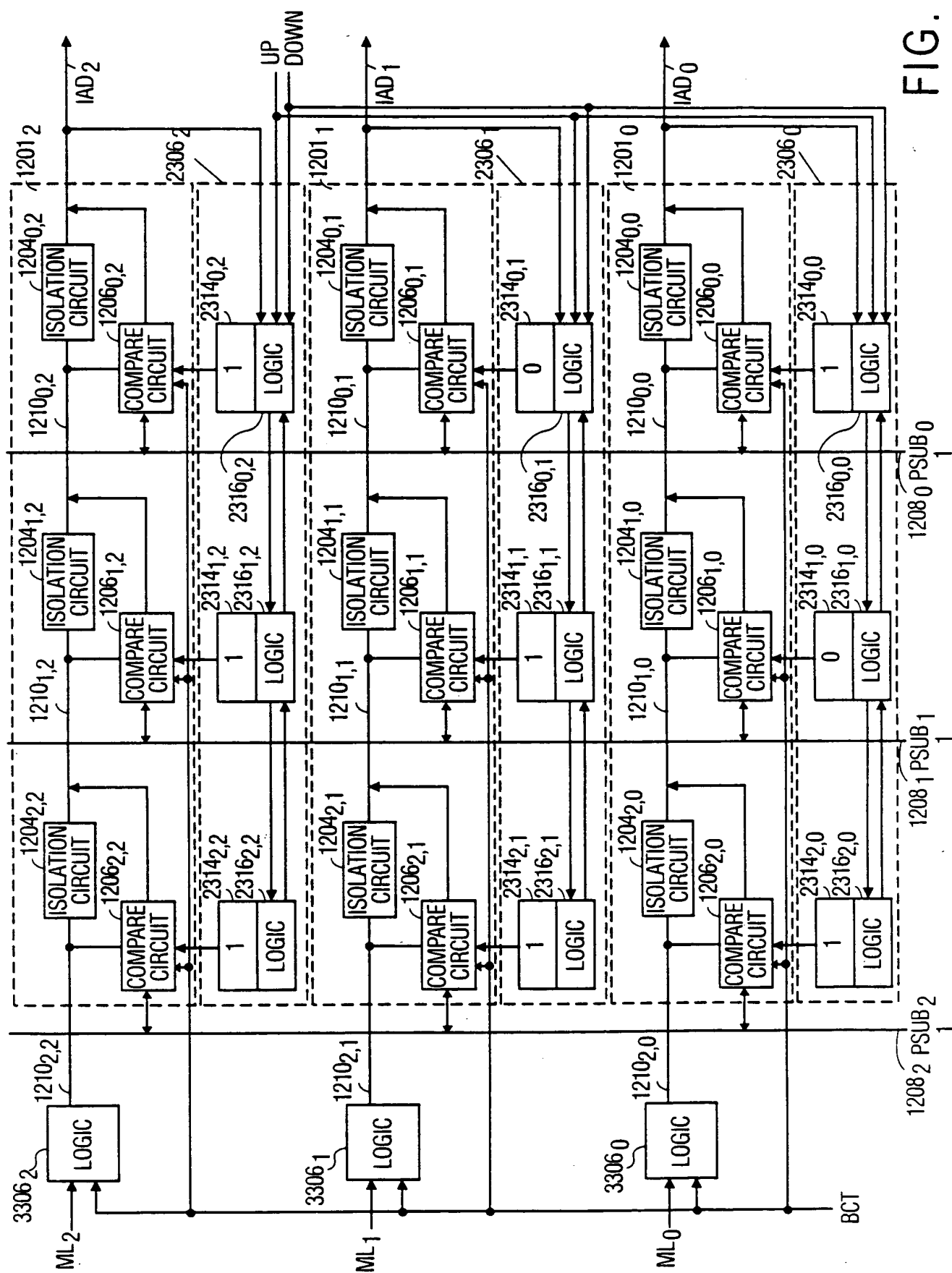


FIG. 37



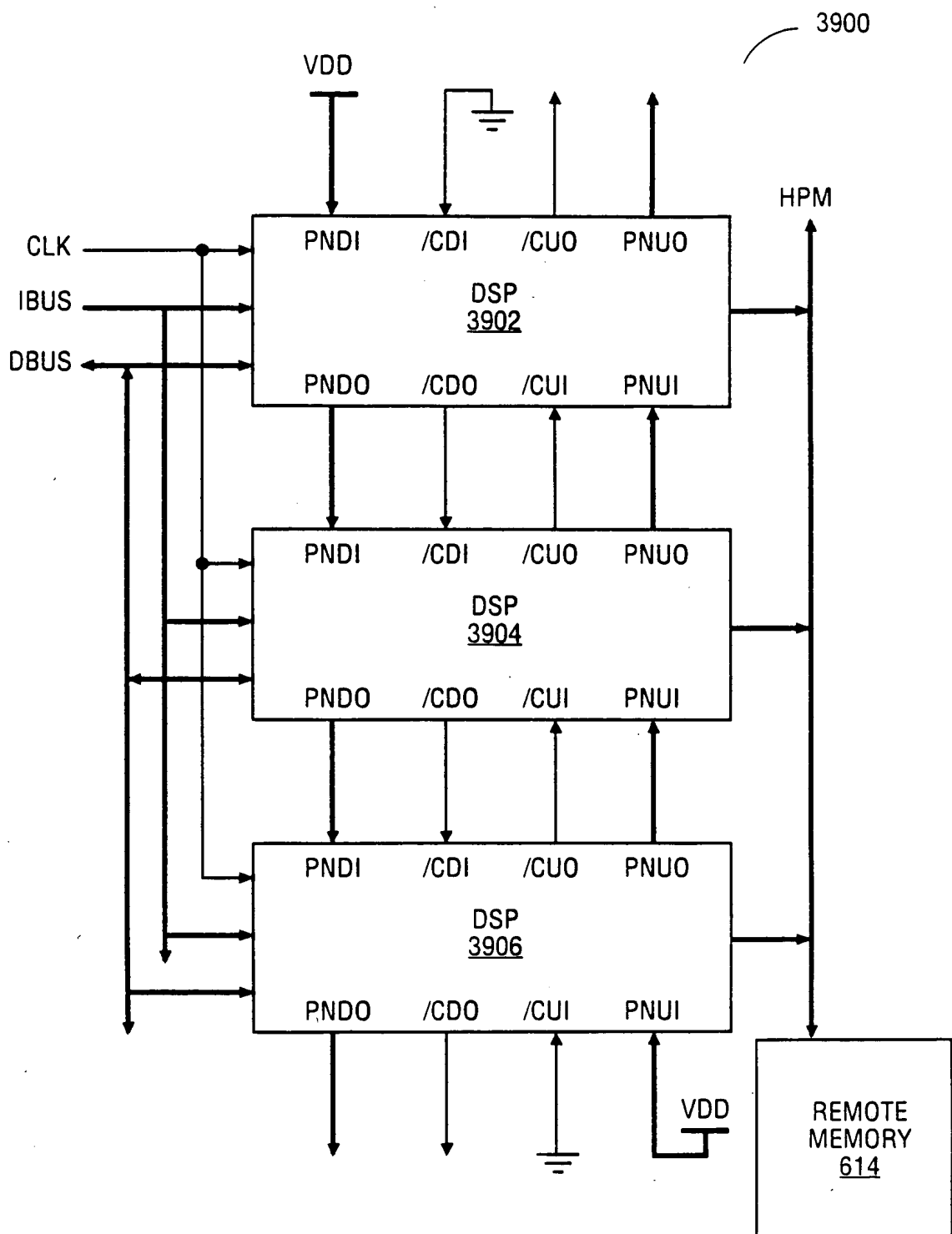


FIG. 39

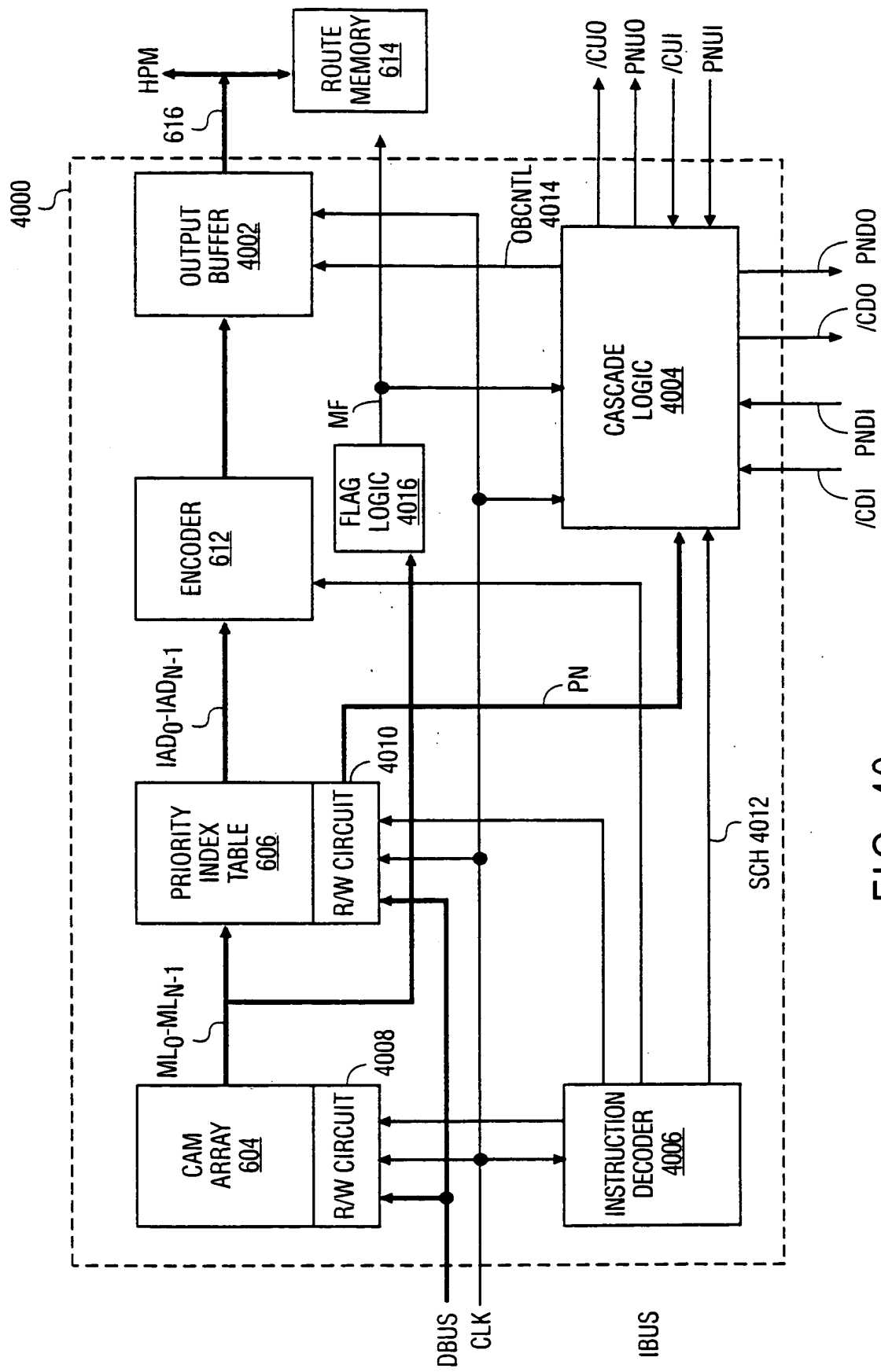


FIG. 40

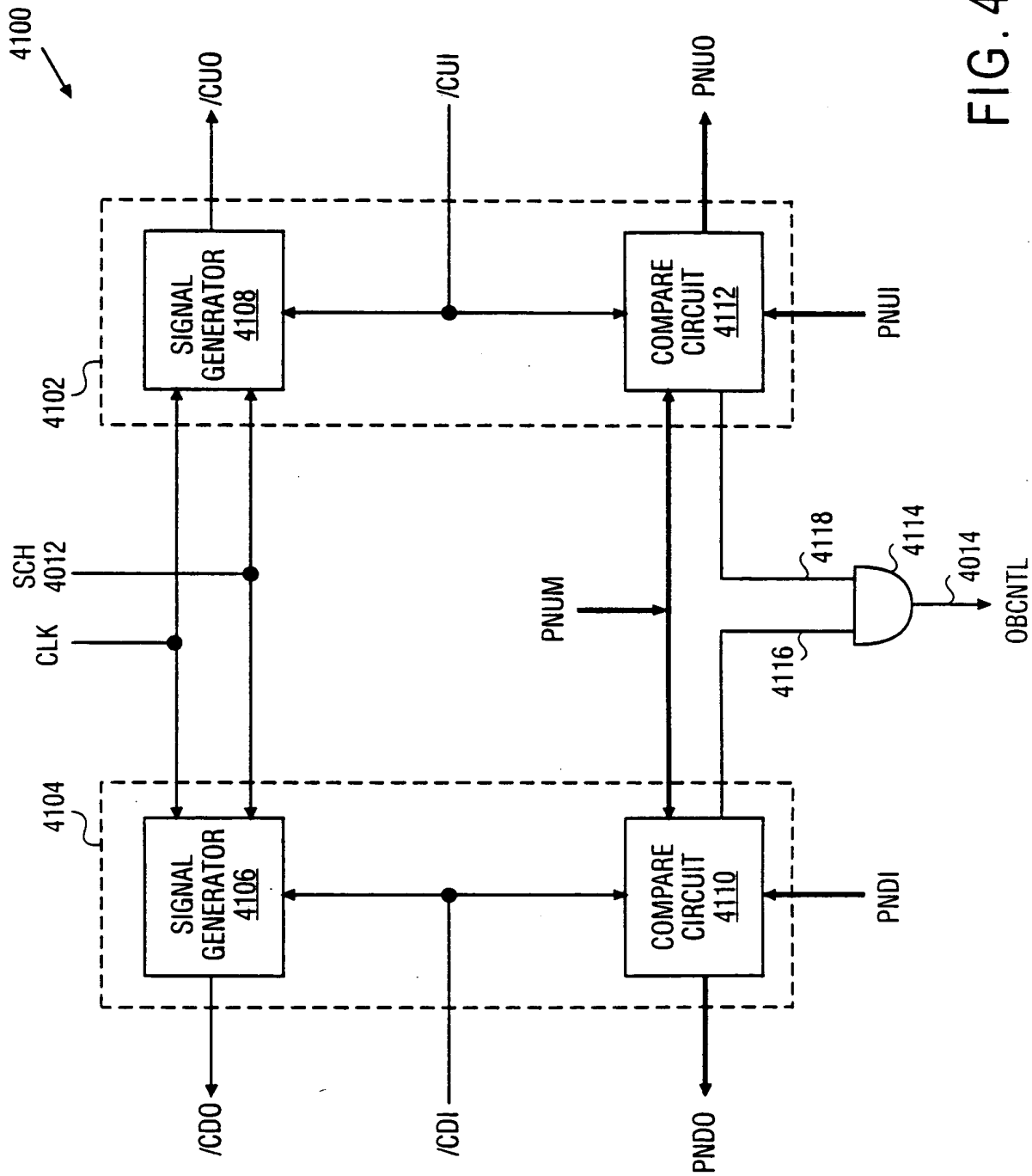


FIG. 41

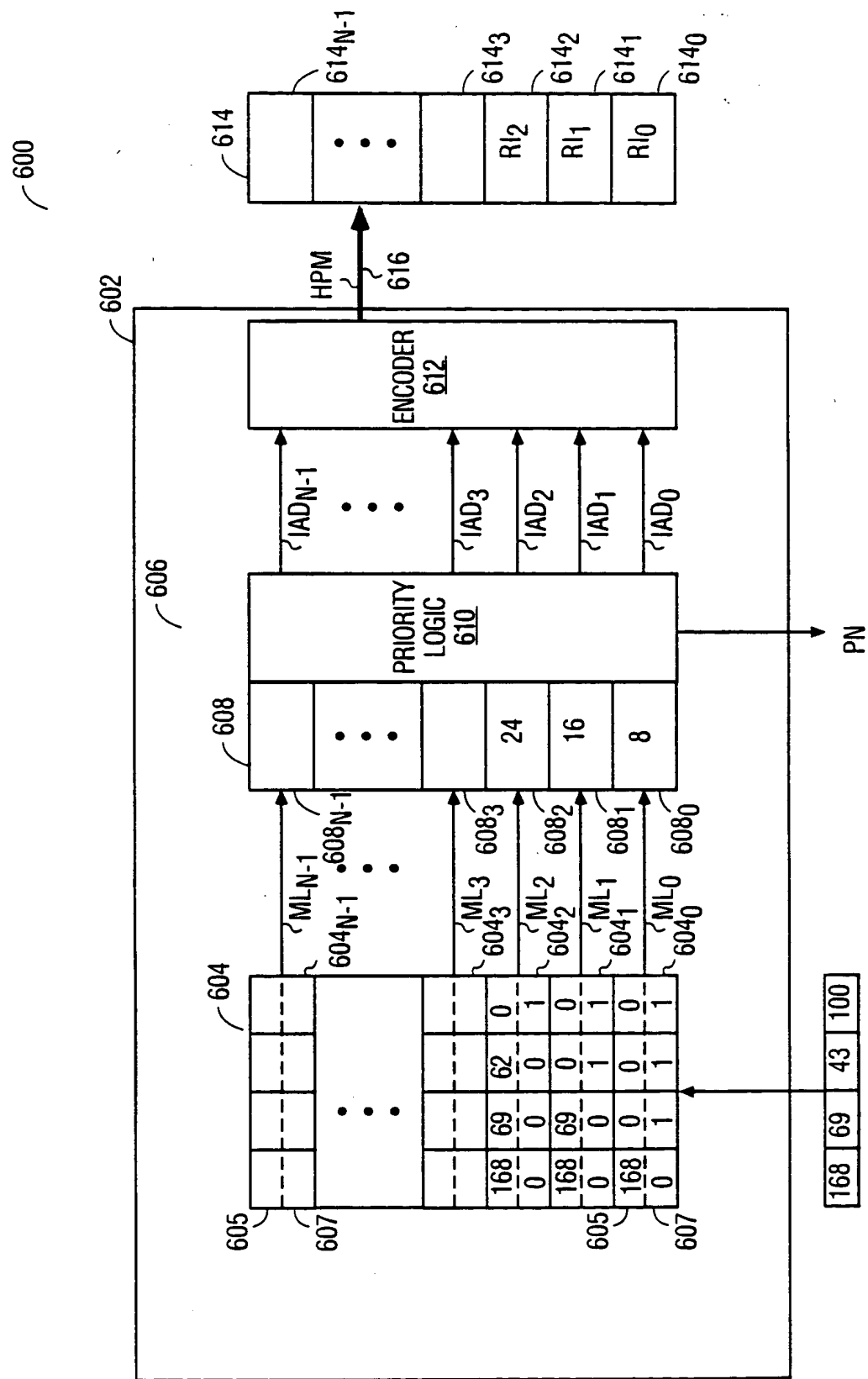


FIG. 42

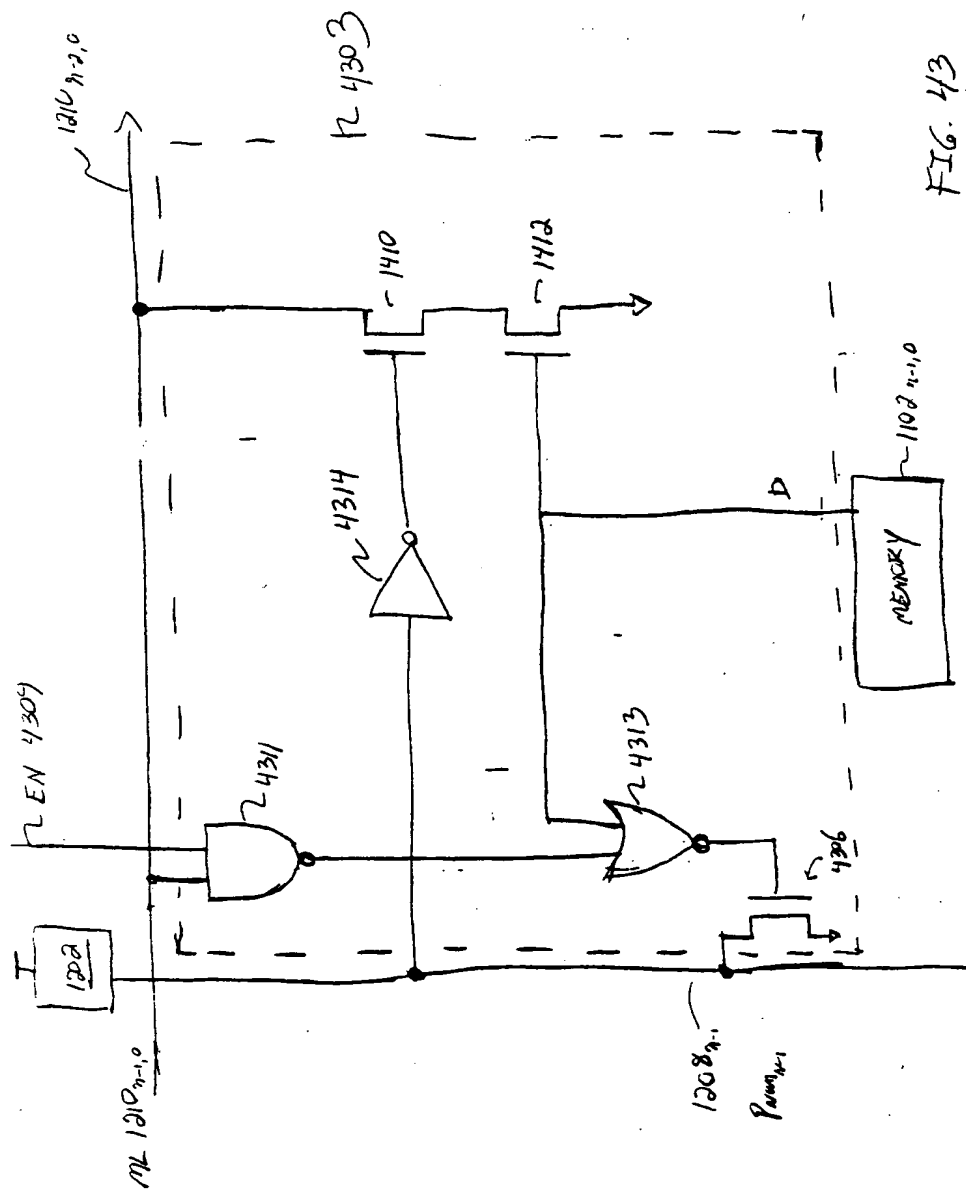


FIG. 43

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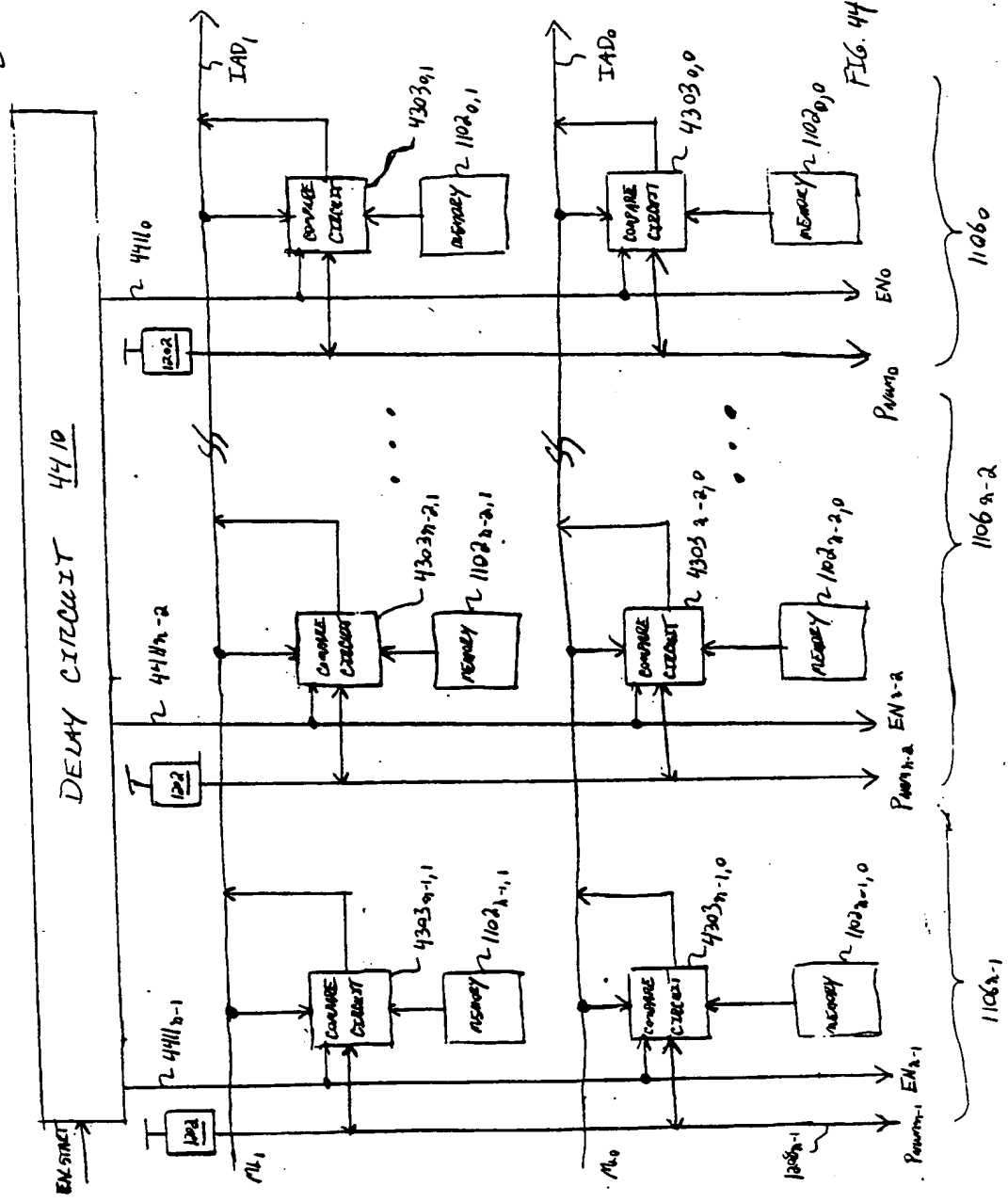
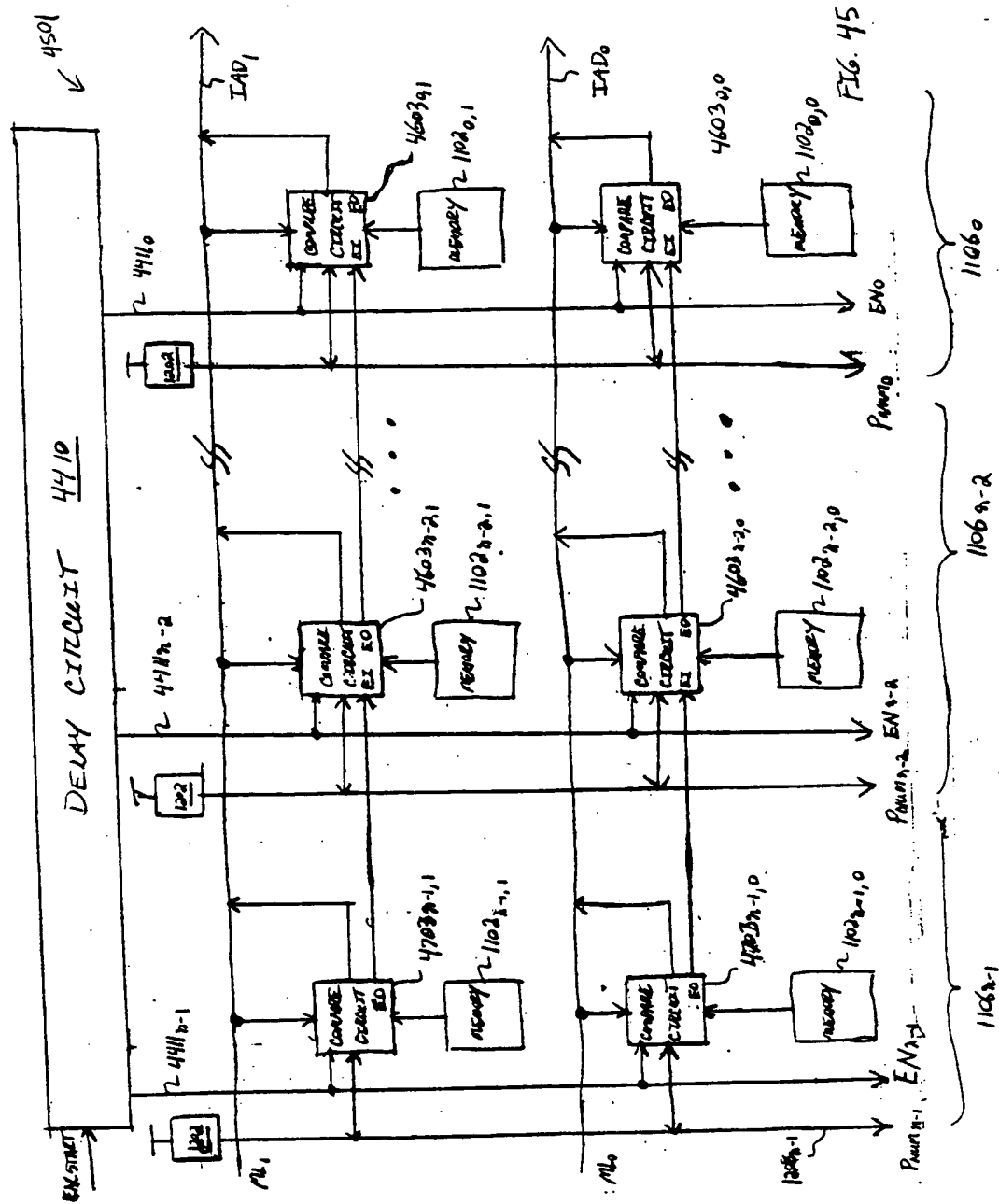
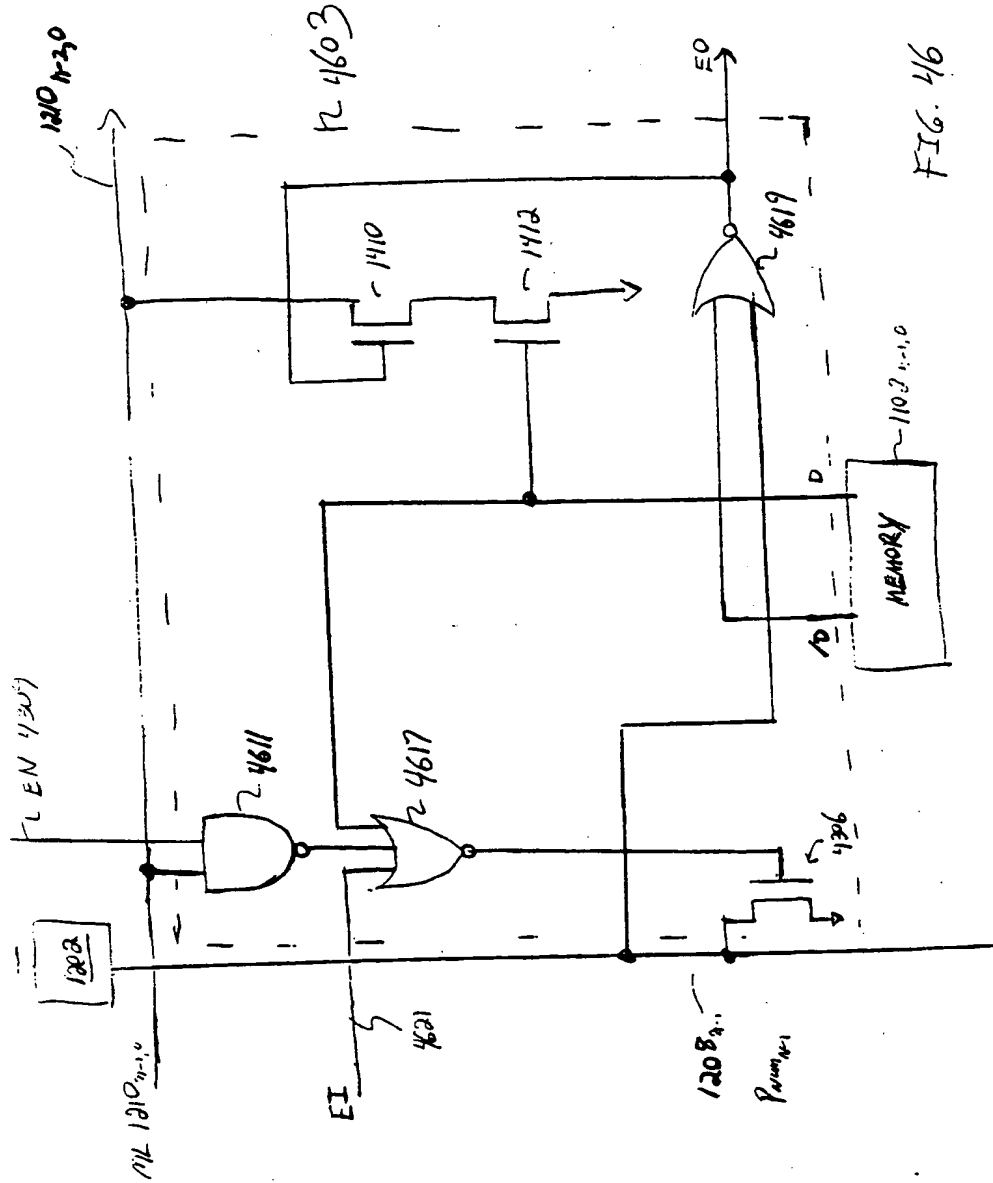


FIG. 44





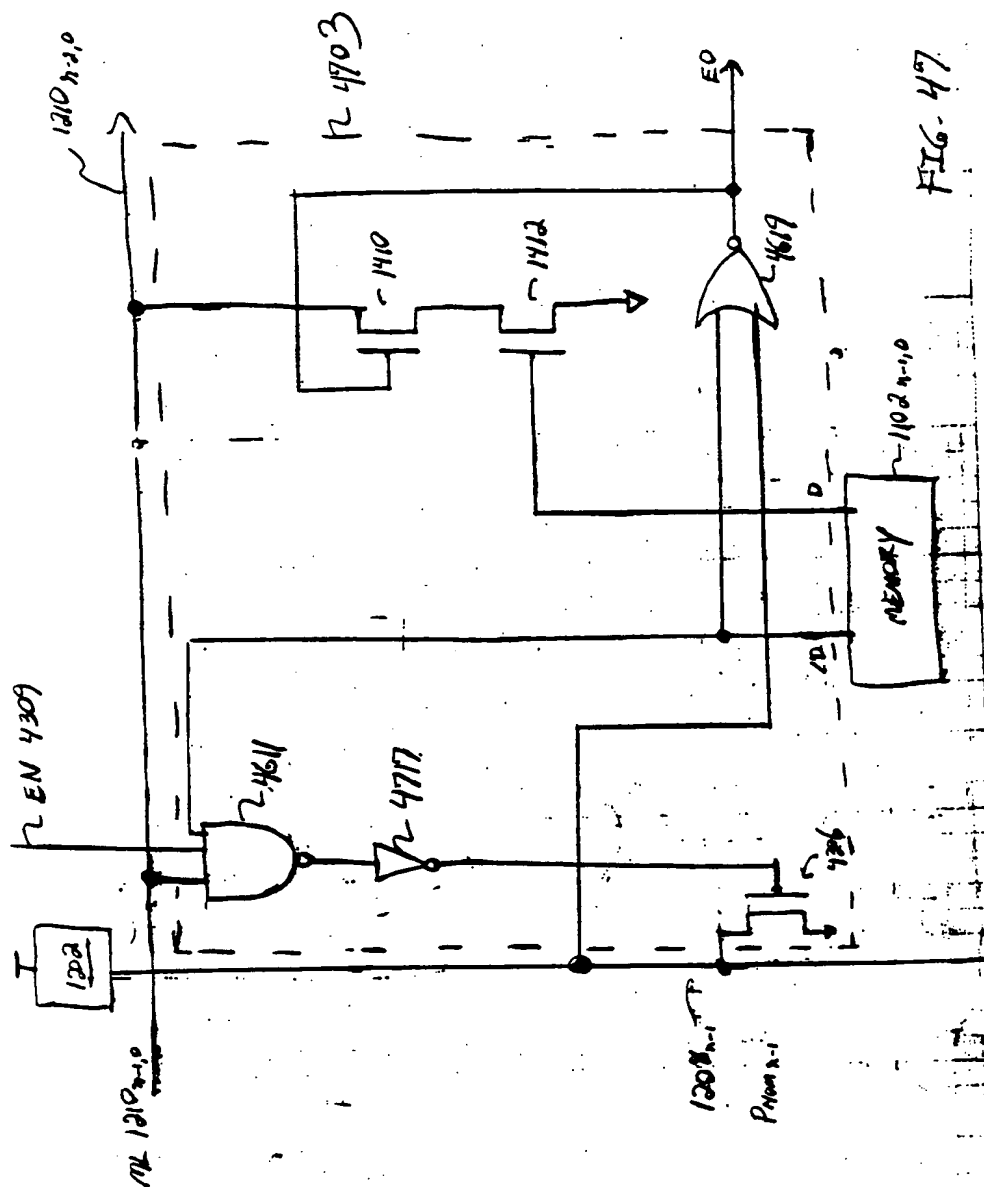


FIG. 47.

DELAY
CIRCUIT

4410

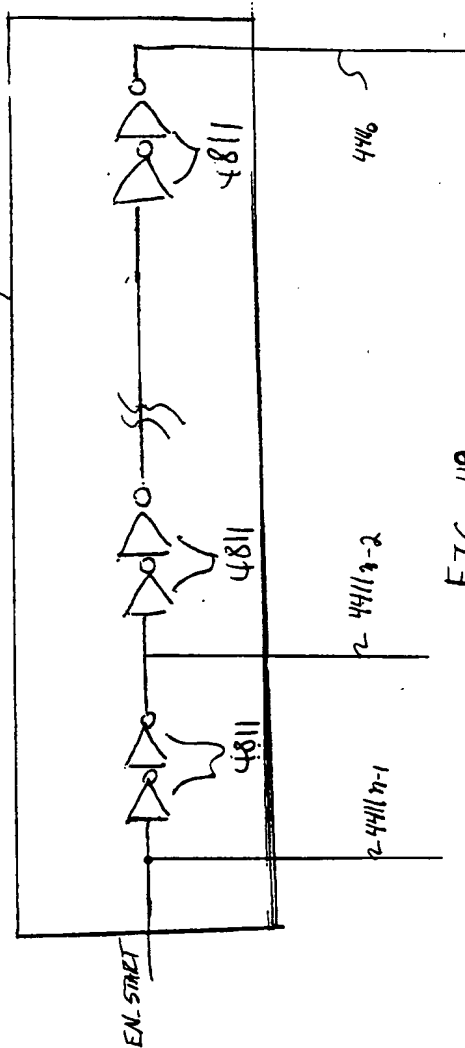


FIG. 4B